King Fahd University of Petroleum & Minerals Electrical Engineering Department EE-390; Exam-1(071); 4th October, 2007

Prob.1	Prob.2	Prob.3	Total

Answer all questions. Time 1.20 hour

Name:	Section	n :	I.D.	
1(a). Write a program that we memory locations stated location of ES:2050 _H .	rting from DS:1050 _H			
Line1:	·	Line8:		
1(b). Using shift-rotate instruct content of the carry flag.			gister with the	existing
Line1:				
Line3:				
Line1:				

2(a). If DS=1000_H, ES=2000_H, SS=3000_H, BP=2345_H, SP=3456_H, SI=9876_H, DI=1122_H and the word <u>memory-contents</u> of following physical address are: $1000:2345_{H} \rightarrow 7823_{H}$, $1000:2347_{H} \rightarrow 1418_{H}$, $1000:2349_{H} \rightarrow 3000_{H}$, $3000:2345_{H} \rightarrow 9867_{H}$, $3000:2347_{H} \rightarrow 6543_{H}$, $3000:2349_{H} \rightarrow 1000_{H}$, $2000:2345_{H} \rightarrow 4AB7_{H}$, $2000:2347_{H} \rightarrow 4C4B_{H}$, $2000:2349_{H} \rightarrow 9000_{H}$,

Execute the following 3-line program and find SI, DI and addressing mode:

LEA BP, [DS:2347_H];
LES SI, [BP];

LDS DI, [ES:2347_H];

- (i) SI = ____
- (ii) DI = _____
- (iii) Addressing mode used in the <u>3rd line</u>:

2(b). IF AX=7F98_H, BX=0087_H, Execute the following 4-line program and find the required value of AX and DX:

ADD AL, BL;

DAA;

ADC AH, 0_H;

CWD;

- (i) AX = _____
- (ii) DX = _____

of AH register, without chang	ging the contents of other bits of AH r	egister.
Line 1:		
Line 2:		_
3(b). 8088 processor has	bit address bus	
3(c) If the CPU registers already h	have the initial values of RX=0710	SI=0750
DI=0730 _H . Without changing	have the initial values of BX=0710_H , So these values, use Indexed memory a	ddressing
DI=0730 _H . Without changing		ddressing
DI=0730 _H . Without changing	g these values, use <u>Indexed</u> memory a with the word content of physical add	ddressing ress= 907A2 _H
DI=0730 _H . Without changing	g these values, use Indexed memory a	ddressing ress= 907A2 _H
DI=0730 _H . Without changing	g these values, use <u>Indexed</u> memory a with the word content of physical add	ddressing ress= 907A2 _H
DI=0730 _H . Without changing	g these values, use <u>Indexed</u> memory a with the word content of physical add	ddressing ress= 907A2 _H
DI=0730 _H . Without changing mode to load the AX register v	g these values, use <u>Indexed</u> memory a with the word content of physical add. Use as many lines as needed to	ddressing ress= 907A2 _H
DI=0730 _H . Without changing mode to load the AX register v	g these values, use <u>Indexed</u> memory a with the word content of physical add	ddressing ress= 907A2 _H
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3(a). Write a <u>2-line</u> program that will TOGGLE the MSB and MASK the LSB contents