Name:_____
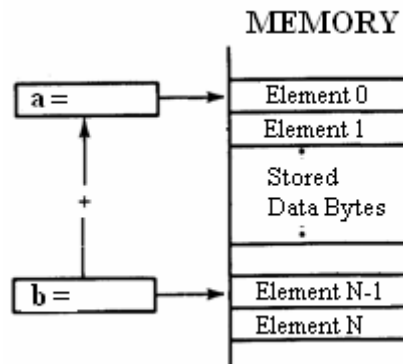
ID No:_____

1. Using <u>Index addressing mode</u>, if the stored data in "element N-1" is to be accessed, which part of offset address should be assigned to 'a' and 'b' of the figure:

    a = _____

    b = _____

MEMORY

a =

Element 0
Element 1
Stored
Data Bytes

b =

Element N-1
Element N

2. If the contents of CPU registers are as follow: **DS**= 2003_H, **ES**= 4043_H, **SS**= 9343_H, **SI**=1008_H , **DI**=1048_H , **BX**=1008_H, **IP**=1012_H, Find the following answers:

    Using <u>Indexed addressing mode</u>, write a program to load **DX** register with the word contents of main memory location **210AA$_H$**.   (*Use two lines of code or less*)

    _____

    _____

    (b) Write a program that will convert the ASCII character (any one of '0_{ASCII}' to '9_{ASCII}') stored in **CL** register into its equivalent decimal value.
                                    (*Use our lines of code or less*)

    _____

    _____

    _____

    _____