

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

Ministry of Higher Education  
King Fahd University of Petroleum & Minerals  
Electrical Engineering Department



وزارة التعليم العالي  
جامعة الملك فهد للبترول والمعادن  
قسم الهندسة الكهربائية

EE 203: Electronics I

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Examination: First Major Exam

Date : October 6, 2013

Time: 5:45-7:15 PM

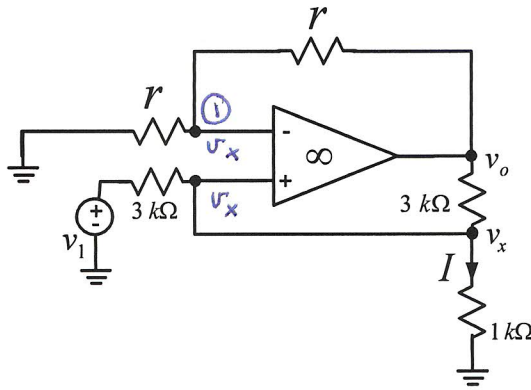
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| Student Name:   | Key |
| Student Number: |     |
| Section Number: |     |

|           |  |
|-----------|--|
| Problem 1 |  |
| Problem 2 |  |
| Problem 3 |  |
| Total     |  |

Answer All Questions

**Problem (1-A) [ points 7]**

For the op amp circuit shown below, find the value of the current  $I$  in terms of  $V_1$



KCL at ①

$$\frac{V_x}{r} + \frac{V_x - V_o}{r} = 0$$

$$V_o = 2V_x \quad \text{--- ①}$$

3 - marks

KCL at  $V_x$

$$\frac{V_x - V_1}{3} + \frac{V_x - V_o}{3} + I = 0$$

3 - marks

$$2V_x - V_1 - V_o + 3I = 0 \quad \text{sub. from ①}$$

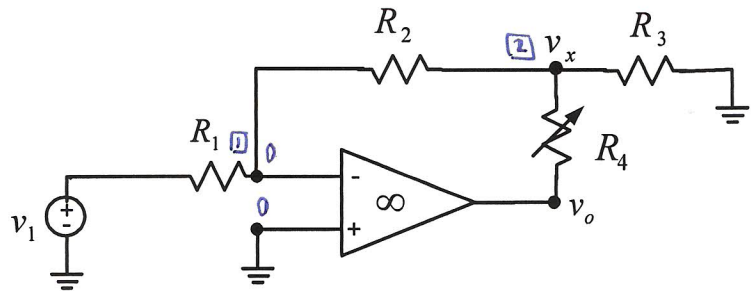
$$3I = V_1$$

$$\boxed{I = \frac{V_1}{3}}$$

1 - mark

**Problem (1-B) [ points 8]**

Design the circuit shown in Fig. 1-b to have an input resistance of  $100\text{ k}\Omega$  and gain that can be varied from  $-1\text{ V/V}$  to  $-10\text{ V/V}$  using  $10\text{-k}\Omega$  potentiometer  $R_4$  ( $R_4$  can vary from  $0$  to  $10\text{ k}\Omega$ ). What voltage gain results when the potentiometer is set exactly at its middle value. (ideal op-amp)



Input resistance =  $R_1 = 100\text{ k}\Omega$

KCL at ①

Fig. 1-b

1 mark

$$\frac{V_1}{R_1} + \frac{V_x}{R_2} = 0 \Rightarrow V_x = -\frac{R_2}{R_1} V_1 \quad \text{--- (1)}$$

2 marks

KCL at ②

$$\frac{V_x}{R_2} + \frac{V_x - V_o}{R_4} + \frac{V_x}{R_3} = 0$$

$$V_o = \left[ 1 + \frac{R_4}{R_2} + \frac{R_4}{R_3} \right] V_x \quad \text{--- (2)}$$

2 marks

From (1) into (2)

$$V_o = -\frac{R_2}{R_1} \left[ 1 + \frac{R_4}{R_2} + \frac{R_4}{R_3} \right] V_1$$

At  $R_4 = 0$   $V_o = -V_1 \Rightarrow R_1 = R_2 \Rightarrow R_2 = 100\text{ k}\Omega$

At  $R_4 = 10\text{ k}\Omega$   $V_o = -\left[ 1 + \frac{10}{100} + \frac{10}{R_3} \right] V_1 = -10 V_1$

2 marks

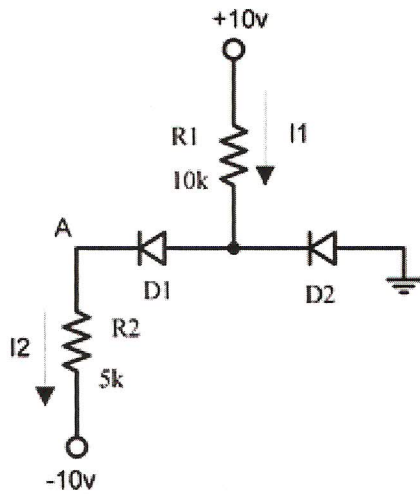
Gain at  $R_4 = 5\text{ k}\Omega$   $1.1 + \frac{10}{R_3} = 10 \Rightarrow \frac{10}{R_3} = 8.9 \Rightarrow R_3 = \frac{10}{8.9} = 1.1236\text{ k}\Omega$

$$\frac{V_o}{V_1} = -\frac{100}{100} \left[ 1 + \frac{5}{100} + \frac{5}{1.1236} \right] = -5.5$$

1 mark

**Problem (2-A) [ 5 points]**

Analyze the diode circuit shown to determine the currents and voltage listed below, assuming constant voltage drop model diode with  $V_{D0} = 0.7 \text{ V}$  (show your analysis):



|            |       |       |
|------------|-------|-------|
| $I_1$ (mA) | 1.07  | (1.5) |
| $I_2$ (mA) | 1.72  | (1.5) |
| $V_A$ (V)  | -1.40 | (2)   |

Assume both diodes are ON

$$V_A = -1.4 \text{ V}$$

$$I_1 \approx \frac{10 + 0.7}{10} \approx 1.07 \text{ mA}$$

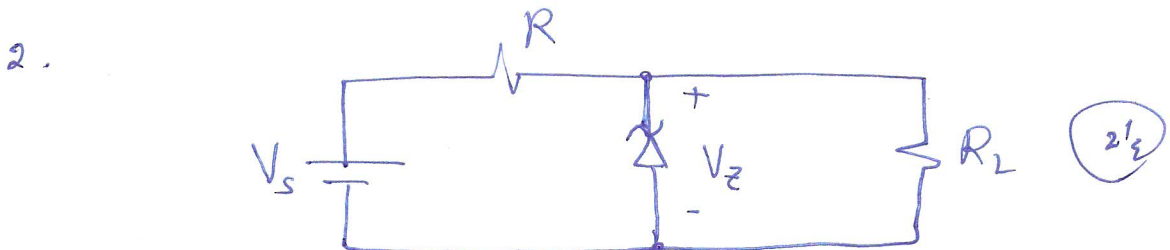
$$I_2 \approx \frac{-1.4 - (-10)}{5} \approx 1.72 \text{ mA}$$

**Problem (2-B) [ 5 points]**

A shunt regulator utilizing a zener diode with an incremental resistance of  $20 \Omega$  is fed through a  $480 \Omega$  resistor; the supply voltage varies by as much as 20% of its nominal values.

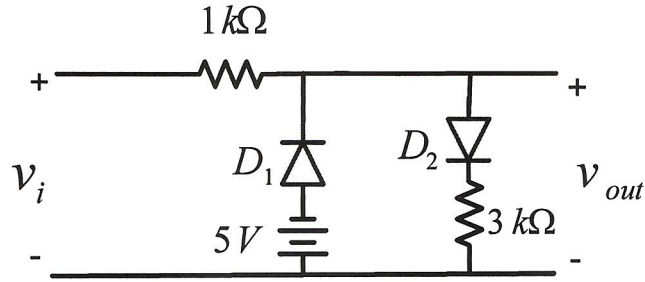
1. Find the line regulation.
2. Draw this regulator circuit including the load which can be represented by a resistor  $R_L$ .

$$1. \quad \frac{\Delta V_o}{\Delta V_s} \approx \frac{r_z}{r_z + R} \approx \frac{20}{20 + 480} \approx 0.04 \quad (2.5)$$



**Problem (2-C) [ 5 points]**

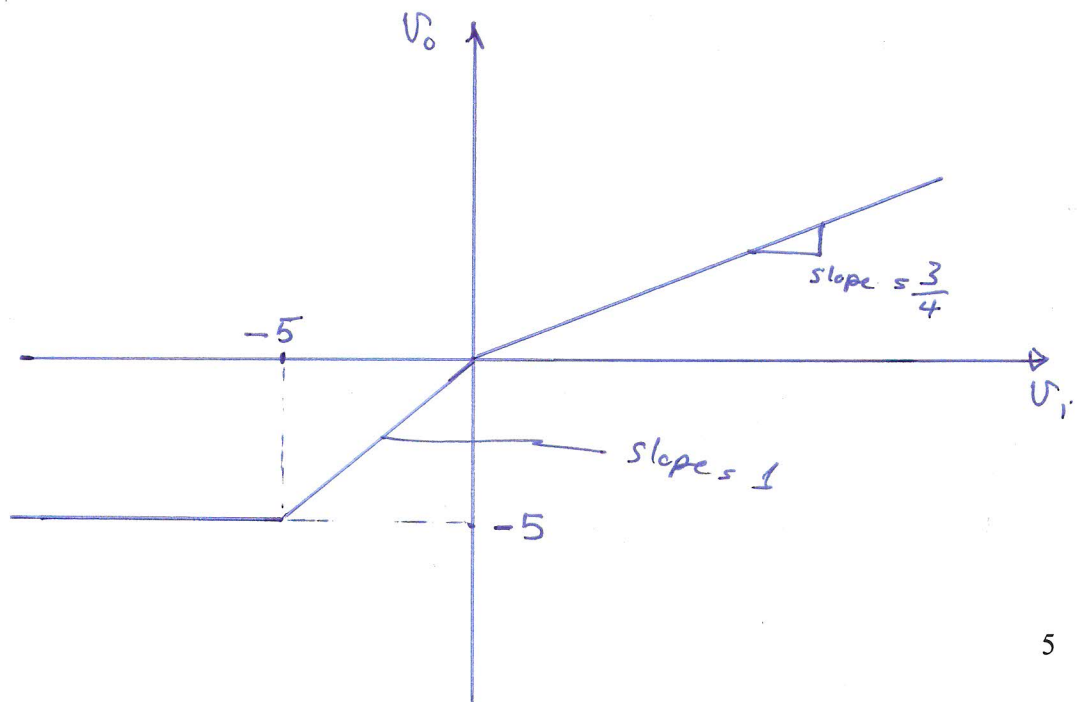
Sketch the transfer characteristics of the circuit shown assume ideal diodes,



If  $V_i \leq -5V$   $D_1$  is ON,  $D_2$  is off marks (1)  
 $V_o = -5V$

If  $-5 < V_i \leq 0$   $D_1$  and  $D_2$  are off mark (1/2)  
 $V_o = V_i$

If  $V_i \geq 0$   $D_1$  off and  $D_2$  is ON marks (2)  
 $V_o = V_i \cdot \frac{3}{4}$



**Problem (3) [ points 15]**

The power supply circuit shown in Fig. 3.1 uses a full wave rectifier to obtain 18 V DC.

- i- Sketch  $v_o$  before the capacitor is connected indicating the significant points.
- ii- Find the ripple voltage.
- iii- Find the value of the smoothing capacitor  $C$ .
- iv- Find the value of the PIV across each diode.

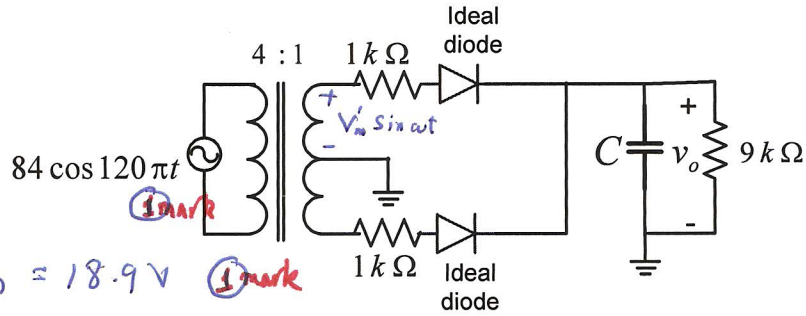
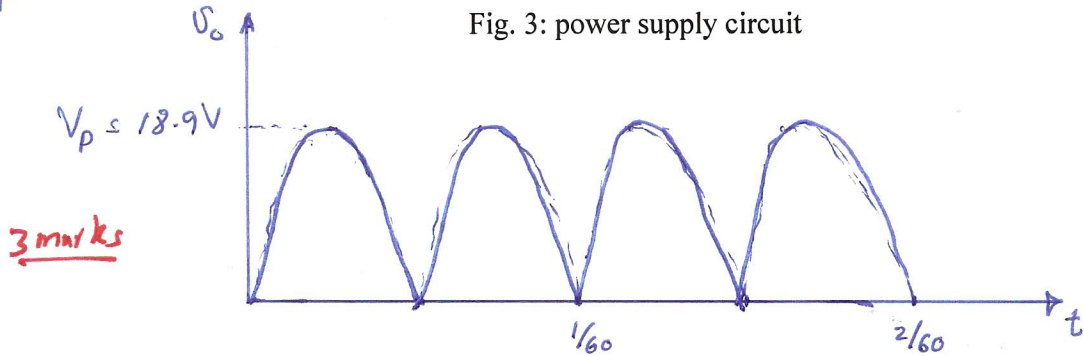


Fig. 3: power supply circuit

(i)  $V'_m = \frac{84}{4} = 21 \text{ V}$  (1 mark)

$V_p = 21 \times \frac{9}{10} = 18.9 \text{ V}$  (1 mark)



3 marks

(ii)  $V_r = \frac{V_p}{2fR_L C}$

$V_{DC} = V_p - \frac{1}{2} V_r$   
 $18 = 18.9 - \frac{1}{2} V_r$  4 marks

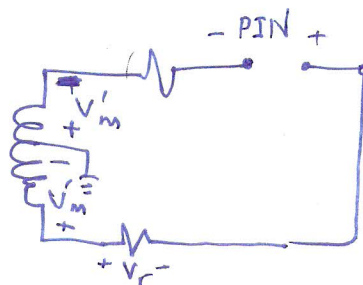
$V_r = 1.8 \text{ V}$

(iii)  $1.8 = \frac{18.9}{2 \times 60 \times 9 \times 10^3 \times C}$

$C = \frac{18.9}{120 \times 9 \times 1.8 \times 10^3} = 9.72 \text{ } \mu\text{F}$  3 marks

(iv)

$PIV = 2V'_m - V_r$   
 $= 42 - (21 \times \frac{1}{10})$   
 $= 39.9 \text{ V}$



3 marks