بسم الله الرحمن الرحيم

King Fahd University of Petroleum and Minerals Department of Electrical Engineering

 Semester I 2006/2007
 Electronic Circuits II (EE303)
 Final Test

ATTEMPT ALL QUESTIONS TIME ALLOWED THREE HOURS

Q1. Consider the amplifier circuit shown in Fig. 1 and answer the following questions:

- a. Calculate the input resistance and the output resistance of the circuit.
- b. Calculate the HF poles of the circuit.
- c. Calculate the LF poles of the circuit.
- d. It is required to reduce the output resistance to half its value and to increase the input resistance to five times its value using the Feedback Principle rather than redesigning the circuit. Suggest the appropriate feedback configuration(s) that can achieve the required goal.
- e. Draw a complete circuit shown your modifications. AT THIS STAGE, DO NOT CALCULATE FEEDBACK RATIO. JUST SHOW THE SUGGESTED CONFIGURATIONS.

Q2. Obtain the transfer function of the circuit shown in Fig. 2. Sketch this transfer function showing the most important characteristics of it. Select design values for the resistors and capacitors so that the center frequency of this transfer function will be located at 10 kHz.

Q3. Consider the circuit shown in Fig. 3 and answer the following questions:

- a. Can this circuit oscillate?
- b. If your answer to (a) is YES, calculate the frequency and the condition of oscillation.
- c. If your answer to (a) is NO, then explain why.

Q4. Design a circuit to implement the following function:

$$v_o = 5v_i + 3\frac{dv_i}{dt} + 2\int v_i dt$$

Draw a complete circuit showing the component values of your design.

Q5. Sketch the output waveform in each the following circuits (See F_{9} . H)

