Section 3

output functions. Draw the logic circuit.

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS Department of Electrical Engineering

EE200 Digital Logic Circuit Design	Quiz # 7	14 January 2008
Student's Name:	I.D.#:	
Design a synchronous sequential circuit whose state di Answer the following:	iagram is given. Use	JK flip flops.
What is the number of external inputs? What is the number of flip-flops? What is the number of outputs?		

Obtain the state and the excitation tables of the circuit. Simplify the flip flop input functions and

Do not write below this line

Instructor: Dr. M.M. Dawoud (071) Score: /10