EE200 Digital Logic Circuit Design	Quiz #6 (23/5/2007)	Section:
Student's Name :		I.D.# :

Derive the state table and the state diagram of a sequential circuit with two JK flip flops, A, and B, one input x and one output y. The circuit is described by the following flip flop input functions:

$$J_A = B \oplus x \qquad K_A = Bx'$$
$$J_B = A \qquad K_B = A'x$$
$$y = AB'x + A'x'$$

Instructor : Dr. M.M. Dawoud   *(062)*

Score :   */10*