EE200 Digital Logic Circuit Design	**Quiz #5 (5/5/2007)**	**Section:**
Student's Name :		**I.D.# :**

Derive the PLA programming table to implement the following two Boolean functions. Minimize the number of product terms. What is the size of the PLA?

$$F_1(x, y, z) = \sum(0,5,6,7) \quad \text{and} \quad F_2(x, y, z) = \sum(1,2,3,5)$$

Instructor : Dr. M.M. Dawoud   *(062)*

Score :   */10*