King Fahd University of Petroleum and Minerals

Electrical Engineering Department

EE460: Power Electronics

Dr. Mahmoud Kassas First Semester 2013-2014 (131)

A. Course Information

Text Book:	Power Electronics: Circuits, Devices and Applications" 4 rd edition, by Rashid, M. H.								
	Name,	,		Off	ice	Phone	Class meeting		Sections
	Email add	ress					(59-2018)		
Instructors:	Dr. Mahmoud Kassas		59/1	081	2271 M-V		V 08:30-	1	
	mkassas@kfupm.edu.s	sa					09:45AM		
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Grading:	Attendance, Assignme	ents and	Quizzes	Proj	ects	s Two M		ors	Final
	15% (2%, 3%,	& %10))	10	10%		30%		25%
	First Major	Se	econd Maj	or	Pr	ojects Due Dates Final		nal	
Exams Dates:	Oct. 8, 2013]	Dec. 10, 2013		Sunday Dec. 23,		, 2013		
Exams Times:	6:00-7:30PM	5	5:00-6:30PN		At 8:30A		M Jan. 6,		2014,
Exams Places:	TBA		TBA				at 8:00AM		
Important	Last day to drop the co	ourse	Last da	y to drop the course		Last day to drop all courses			
Dates:	without a permanent r	ent record w		ith "W" grade		with "W" Thru Registrar's			
							office.		
	Sept. 12, 2013		O		Oct. 21, 2013		Nov. 21, 2013		
Field Trip	To be arranged later								

Course Objectives:

After successfully completing the course, the students will be able to:

- Understand the theory and methods for analysis and design of power electronics circuits,
- Applications of solid-state devices for the control and conversion of electric energy.
- Know how to use design and simulation software such as Pspice and Matlab.

Outcome Coverage:

- (a) An ability to apply math, science and engineering knowledge. The homework, project, quizzes and exams require direct applications of mathematical, scientific, and engineering knowledge to successfully complete the course.
- (b) An ability to design and conduct experiments, as well as to analyze and interpret *data*. The homework and project require student to design, conduct simulations using Pspice or MATLAB and analyze simulation data.
- (c) An ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability. The design in the project must be checked against real world operating limits.
- (d) *Identify, formulate and solve engineering problems.* Students must be able to identify and model the system; analyze and solve control problems.
- (e) An ability to communicate effectively. Students are required to write a comprehensive report on the project.

(f) An ability to use the techniques, skills, and modern engineering tools necessary for engineering *practice*. Students taking the course will learn how to use power electronics techniques and software tools such as Pspice and MATLAB for solving practical control problems.

Important Remarks:

- <u>Attendance:</u> 1/3 point will be deducted for each unexcused absence, starting from the first absence. Any student that exceeds 20% of the schedule class meeting <u>without any official excuse</u> will receive a grade **DN** in the course.
- <u>Official excuses:</u> All official excuses must be submitted to the instructor <u>no later</u> <u>than one week</u> of the date of the official excuse. The instructor may not accept a late excuse.
- <u>Academic Dishonesty:</u> According to the university regulations, if the instructor for any instance of academic dishonesty discovers any student, the instructor may give a <u>failing (F) grade</u> to the student and <u>report it to the dean of the college.</u>
- <u>Homework:</u> The homework assignment is to be solved completely by the students. However, homework solution will not be collected. Instead, quiz related to the homework problems is expected on Wednesdays, in the week following the homework assignment date. Homework solution can be found on the network link: <u>WebCT</u>

Office Hours: Monday-Wednesday 10:00-11:00AM, Tuesday 2:00-3:00PM or by appointment.

KING FAHD UNIVERSITY OF PETROLEUM AND MINERALS ELECTRICAL ENGINEERING DEPARTMENT

EE 460 – Power Electronics

Tentative Schedule [131]

DATE	TOPICS	SECTIONS	HW	LABORATORY							
Week 1 Sept. 1-5	Introduction to Power Electronics & Semiconductor Diodes	2.1, 2.3, 2.4, 2.5, 2.10	Ch. 2-1, 10, 12(b,c,d)								
Week 2 Sept. 8-12	Diode Circuit & Rectifiers	3.2, 3.3, 3.4,	Ch 3 1, 5, 7, 9,								
Week 3 Sept. 15-19	Diode Circuit & Rectifiers	3.7, 3.10	Ch 3 11, 15, 21	Lab 1: Pspice and COM3LAB Kit, Single phase half wave rectifier							
Week 4 Sept. 22- 26	Thyristors	7.2, 7.3, 7.7, 7.5, 7.9,7.10	Ch 7 1, 3	Lab 2: Three-phase bridge rectifier, (Pspice)							
Week 5 Sept. 29 – Oct. 3	Controlled Rectifiers	10.4	Ch10 1, 2, 7, 8,	Lab 3 Three-phase bridge rectifier. (Experiment)							
Week 6 Oct. 6 – 9	Controlled Rectifiers	10.9	Ch 10 22, 23	Lab 4: Single-phase controlled bridge rectifier							
Eid Al-Adha Holiday											
Week 7 Oct. 21 – 24 Major - I	AC Voltage Controllers	11.2, 11.4,	Ch 11 1, 3, 6,	Lab 5: Three-phase controlled bridge rectifier (PSpice)							
Week 8 Oct. 27 – Oct. 31	AC Voltage Controllers	11.5, 11.12	Ch 11 8, 9, 28								
Week 9 Nov. 3 – 7	Power Transistors	4.2, 4.3, 4.7	Ch 4 1, 2, 11	Lab 6: Three-phase controlled bridge rectifier (Experiment)							
Week 10 Nov. 10 – 14	DC-DC Converters	4.7, 5.2,	Ch 4 2, 3,	Lab 7: Single-phase AC voltage controller (Pspice)							
Week 11 Nov. 17-21	DC-DC Converters	5.3, 5.4	Ch 5 4, 6	Lab 8 Single-phase ac- voltage controller (Experiment)							
Week 12 Nov. 24 - 28	PWM Inverters	6.2, 6.3, 6.4	Ch 6 2, 3,	Lab 9: Design of a DC chopper							
Week 13 Dec. 1 - 5 Major-II	PWM Inverters	6.5, 6.6,	Ch 6 4,5,								
Week 14 Dec. 8 – 12	PWM Inverters	6.8, 6.10, 6.11	Ch 6 6	Lab 10: Three-phase voltage source inverter							
Week 15 Dec. 15 – 19	Resonant Pulse Inverters	8.2.1, 8.2.2	Ch 8 1, 3	Lab Final							
Week 16 Dec. 22 – 24	Review										