King Fahd University of Petroleum & Minerals Dhahran, 31261 P.O. Box 49 Saudi Arabia Phone +966(5)566-1625 Fax +966(3)860-5725 E-mail saiarios@ccse.kfupm.edu.sa

## Uthman Al-Saiari

Objective	To pursue higher studies (PhD) & research
Education	1993 - 2003King Fahd University of Petroleum & Minerals Computer Engineering Department, Dhahran, SA
	B. S. Degree in Computer Engineering in Dec 1998
	• GPA 3.706 on 4.0 scale
	• Top 2% of my class and was on the Dean's list
	Projects undertaken in areas pertaining to:
	Performance Evaluation of GaAs Circuits
	• Fault Tolerance in CMOS VLSI Circuits
	Internet Search Engines Techniques
	• Videoconferencing over the Internet: Performance Optimization
	M. S. Degree in Computer Engineering (Defended in Aug 2003)
	• GPA 3.688 on 4.0 scale
	• M. Sc. Thesis title "Digital Circuit Design Through Simulated Evolution (SimE) Algorithm" under Prof. Sadiq M. Sait and Prof. Mostafa Abd-El-Barr
	Other projects completed:
	• Novel Design of Multiplier Accumulator (MAC) using 4,2 Adders
	Sequential Circuits Optimization for Power and Delay
	System-on-chip Testing
	Fault Tolerance in Multi-Computer Networks
	Efficient Location Management Technique for Mobile Computing
	Enhancing Security in GSM Network
Publications	• Sadiq M. Sait, Mostafa Abd-El-Barr, <b>Uthman Al-Saiari</b> , Bambang A. B. Sarif, <u>"Digital Circuit Design Through Simulated Evolution (SimE)"</u> . IEEE Congress on Evolutionary Computation, Canberra, pp. 375- 381, July 2003.
	<ul> <li>Mostafa Abd-El-Barr, Sadiq M. Sait, Bambang A. B. Sarif, Uthman Al-Saiari, <u>"A Modified Ant Colony Algorithm for Evolutionary Design of Digital Circuits"</u>. IEEE Congress on Evolutionary Computation, Canberra, pp. 708- 715, July 2003.</li> </ul>
	• Sadiq M. Sait, Mostafa Abd-El-Barr, <b>Uthman Al-Saiari</b> , Bambang A. B. Sa <u>"Fuzzified Simulated Evolution Algorithm for Combinational Digital Logic</u> <u>Design Targeting Multi-Objecitve Optimization"</u> . IEEE ISCAS 20 Submitted Sep. 2003.
	<ul> <li>Mostafa Abd-El-Barr, Sadiq M. Sait, Bambang A. B. Sarif, Uthman Al-Saia <u>"Cominational Logic Circuit Design Through Ant Colony Optimization</u> <u>Algorithm"</u>. IEEE ISCAS 2004, Submitted Sep. 2003.</li> </ul>

Experience	Oct 2001 – Present King Fahd University of Petroleum & Minerals Computer Engineering Department, Dhahran, SA		
	Graduate Assistant		
	<ul> <li>Teaching Experience</li> <li>Taught in the following laboratory courses: <ul> <li>Introduction to Computer Engineering (Teaching and FPGA Lab Manual Development)</li> <li>Digital Systems Design</li> <li>Computer Organization &amp; Architecture</li> </ul> </li> </ul>		
		Nov 1997 – Oct 2001 Saudi Aramco (Oil Company), Dhahran, SA	
		<ul> <li>Saudi Aramco Sponsored Student Nov 1997 - Feb 1999</li> <li>Sponsored by ARAMCO for the B.Sc in Computer Engineering from KFUPM</li> <li>System Analyst Feb 1999-Oct 2001</li> <li>Worked on several projects including: <ul> <li>Developed database applications and web enabled applications</li> <li>Involved in several team projects</li> <li>Set up local area networks in remote areas</li> <li>Worked on handheld PCs and PDA Wireless Application</li> </ul> </li> <li>Summer 1998 Saudi Aramco (Oil Company), Dhahran, SA</li> </ul>	
	Tools Competence		• Programming Languages: C, Pascal, Fortran, VB and Java
			• VLSI tools: VHDL, SIS and Magic, etc
			• Others: MATLAB, Web Development tools and Database Applicatio
	Memberships		IEEE, IEEE Computer Society and IEEE Neural Networks Society.
References	Personal References available upon request.		