



ISCAS 2004

2004 IEEE International Symposium on Circuits and Systems



Sunday, 23 May 2004 – Wednesday, 26 May 2004
Sheraton Vancouver Wall Centre Hotel
Vancouver, British Columbia, Canada

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Conference Management Services

Monday, February 16, 2004

Sadiq Sait Mohammed,
King Fahd University of Petroleum & Minerals
Saudi Arabia

Dear Sadiq Sait Mohammed,

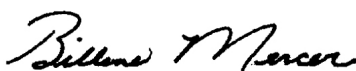
Regarding: FAST FORCE-DIRECTED /SIMULATED EVOLUTION
HYBRID FOR MULTIOBJECTIVE VLSI CELL PLACEMENT

The IEEE Circuits and Systems Society and the ISCAS 2004 Organizing Committee invite you to present your accepted paper at this leading symposium. This year's technical program features over 1270 technical papers spanning all aspects of circuits and systems theory and practice. The symposium program will also include a rich variety of keynote and plenary speakers, tutorials, exhibits, and social activities.

The technical program incorporates papers on all aspects of circuits and systems including: Analog Signal Processing, Biomedical Circuits and Systems, Blind Signal Processing, Cellular Neural Networks and Array Computing, Circuits and Systems for Communications, Computer-Aided Network Design, Digital Signal Processing, Graph Theory and Computing, Multimedia Systems and Applications, Nanoelectronics and Gigascale Systems, Neural Systems and Applications, Nonlinear Circuits and Systems, Power Systems and Power Electronic Circuits, Sensory Systems, Visual Signal Processing and Communications, and VLSI Systems and Applications.

Please join us at the 2004 IEEE International Symposium on Circuits and Systems, May 23-26, 2004 in Vancouver, British Columbia, Canada. You may obtain more information from the web at: <http://www.iscas2004.org> or please feel free to contact me.

Sincerely yours,


Billene Mercer
Conference Manager
ISCAS 2004

www.iscas2004.org