ICS 233 – Computer Architecture & Assembly - Term 082 – Spring 2009

Pipelined Processor Project Evaluation Form

	Student Name / ID	Grade	Remarks	Bonus/Penalty
Members				

	Section	Grade	Max	Remarks
·	ALU Implementation		10	
l (40 pts	Register File Implementation		5	
Components and Control (40 pts)	Correct implementation of individual ALU instructions (R-type and I-type)		10	Instructions are tested separately
ents and	Correct implementation of LW and SW instructions.		5	
ompone	Correct implementation of BEQ, BNE, and J instructions		5	
O	Correct implementation of JAL and JR instructions		5	
(s	Pipelined Datapath and Control		10	
g (40 pt	Forwarding implemented properly		10	
Pipelining (40 pts)	Stalling pipeline (load delay, branch)		10	
P	Enough test cases provided		10	
	Writing Style, Spelling, Grammar		5	
(20 pts)	Design Description & Drawings		5	
Report (20 pts	Test cases, verifying correct execution		5	
	Describing group work, meetings, and contributions of the group members		5	