## ICS 233 – Computer Architecture & Assembly Language

Assignment 3: Integer Multiplication and Division, Floating-Point

1. (3 pts) Using the refined signed multiplication hardware, show the multiplication of:

```
Multiplicand = 00101101 by Multiplier = 11010110
```

The result of the multiplication should be a 16 bit signed number in HI and LO registers. Eight iterations are required because there are 8 bits in the multiplier. Show the steps.

2. (3 pts) Using the refined division hardware (slides 24-25), show the unsigned division of:

```
Dividend = 11101101 by Divisor = 00110110
```

The result of the division should be stored in the Remainder and Quotient registers. Eight iterations are required. Show the steps.

- 3. (2 pts) What is the decimal value of the following single-precision floating-point numbers?
- **4.** (2 pts) Show the IEEE 754 binary representation for: -75.4 in ...
  - a) Single Precision
  - b) Double precision
- - a) x + y
  - **b)** x \* y
- - a) x + y
  - **b)** Result of (a) + z
  - **c)** Why is the result of **(b)** counterintuitive?
- **7.** (**2 pts**) IA-32 offers an 80-bit extended precision option with a 1 bit sign, 16-bit exponent, and 63-bit fraction (64-bit significand including the implied 1 before the binary point). Assume that extended precision is similar to single and double precision.
  - a) What is the bias in the exponent?
  - **b)** What is the range (in absolute value) of normalized numbers that can be represented by the extended precision option?