

COE 301 – Computer Organization

Term 181 – Fall 2018

Pipelined Processor Evaluation Form

Student Name / ID	Grade	Remarks	Bonus/Penalty

Section	Grade	Max	Remarks
Implementing the basic pipelines of datapath and control by inserting needed registers and testing it		25	
Implementing forwarding control to handle data hazards and testing it		25	
Implementing stall control for handling lw and control hazards and testing it.		20	
Report document properly describes the final implementation of the processor pipeline and control, hazard detection, forwarding, and stalling. Sufficient drawings and description is provided for all components, pipeline stages, and control. Sufficient test cases and verification done.		30	