

Lecture 10

5/10/20

Ex 5.1.1

$$V = \begin{bmatrix} 5+3i \\ 6i \end{bmatrix}$$

$$|V| = \sqrt{70}$$

$$\frac{V}{\sqrt{70}} = \begin{bmatrix} \frac{5+3i}{\sqrt{70}} \\ \frac{6i}{\sqrt{70}} \end{bmatrix} = \frac{5+3i}{\sqrt{70}} |0\rangle + \frac{6i}{\sqrt{70}} |1\rangle$$

$$P(0) = \frac{34}{70}, \quad P(1) = \frac{36}{70}$$

$$\star \frac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ 1 \end{bmatrix} = \frac{1}{\sqrt{2}} |0\rangle + \frac{1}{\sqrt{2}} |1\rangle = \frac{|0\rangle + |1\rangle}{\sqrt{2}}$$

$$\star \frac{1}{\sqrt{2}} \begin{bmatrix} 1 \\ -1 \end{bmatrix} = \frac{1}{\sqrt{2}} |0\rangle - \frac{1}{\sqrt{2}} |1\rangle = \frac{|0\rangle - |1\rangle}{\sqrt{2}}$$

$$\begin{aligned} \frac{|0\rangle + |1\rangle}{\sqrt{2}} &= \frac{|1\rangle + |0\rangle}{\sqrt{2}} \\ \frac{|0\rangle - |1\rangle}{\sqrt{2}} &= \frac{|1\rangle - |0\rangle}{\sqrt{2}} \end{aligned}$$

$\frac{1}{\sqrt{2}} \begin{bmatrix} -1 \\ 1 \end{bmatrix}$

$$2^8 = 256 \text{ possible values}$$

A byte is 8 bits

$$B = 01101011$$

$$[0], [1], [1], [0], [1], [0], [1], [1]$$

$$|0\rangle \otimes |1\rangle \otimes |1\rangle \otimes |0\rangle \otimes |1\rangle \otimes |0\rangle \otimes |1\rangle \otimes |1\rangle$$

$$|0\rangle^{\otimes 2} \otimes |1\rangle^{\otimes 2} \otimes \dots \otimes |1\rangle^{\otimes 8} = (|0\rangle^{\otimes 8})^{\otimes 8}$$

$$256 \text{ bits} \quad 2^8 = 256$$

$$\begin{bmatrix} 00000000 \\ 00000001 \\ \vdots \\ 01101011 \\ \vdots \\ 11111111 \end{bmatrix} = B$$

$$\begin{bmatrix} c_0 \\ c_1 \\ c_2 \\ \vdots \\ c_{255} \end{bmatrix} \text{ qubyte}$$

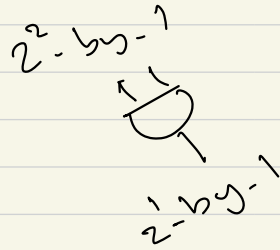
$$\frac{-i}{\sqrt{2}}, \frac{i}{\sqrt{2}}, \frac{1}{\sqrt{2}}, \frac{1}{\sqrt{2}}$$

$$\sum_{i=0}^{255} |c_i|^2 = 1$$

* A qubit pair can be written

$$|0\rangle \otimes |1\rangle \text{ or } |0 \otimes 1\rangle \text{ or } |0\rangle|1\rangle \text{ or } \underline{|01\rangle}$$

$$|01\rangle \begin{matrix} 00 \\ 01 \\ 10 \\ 11 \end{matrix} \begin{bmatrix} 0 \\ 1 \\ 0 \\ 0 \end{bmatrix}$$



Ex

$$\frac{1}{\sqrt{3}} \begin{bmatrix} 1 \\ 0 \\ -1 \\ 1 \end{bmatrix} = \frac{1}{\sqrt{3}} |00\rangle - \frac{1}{\sqrt{3}} |10\rangle + \frac{1}{\sqrt{3}} |11\rangle$$

* The general state of two-qubit system can be written as

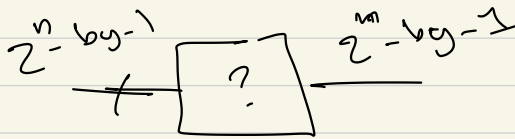
$$|\psi\rangle = c_{00} |00\rangle + c_{01} |01\rangle + c_{10} |10\rangle + c_{11} |11\rangle$$

$$\underline{|0 \otimes 1\rangle} \neq \underline{|1 \otimes 0\rangle}$$

Sec 5.2 Classical Gates

- * Gates are ways to manipulate bits
- It takes bits as input, and output other bits

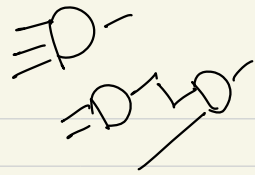
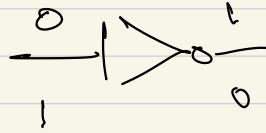
- A gate can take 2^n -by-1 matrix and produces a 2^m -by-1 matrix



- * A gate can be 2^m -by- 2^n

$$\underbrace{(2^m \text{-by-} 2^n)}_{\text{gate}} (2^n \text{-by-} 1) = (2^m \text{-by-} 1)$$

* NOT gate



Not of $|0\rangle$ is $|1\rangle$
 Not of $|1\rangle$ is $|0\rangle$

A	0
0	1
1	0

$$\text{NOT} = \begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix}$$

$$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} 1 \\ 0 \end{bmatrix} = \begin{bmatrix} 0 \\ 1 \end{bmatrix}$$

$\underbrace{\hspace{10em}}_{|0\rangle} \qquad \underbrace{\hspace{10em}}_{|1\rangle}$

$$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 1 \\ 0 \end{bmatrix}$$

AND gate

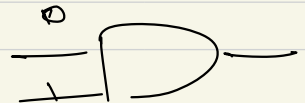


A	B	0
0	0	0
0	1	0
1	0	0
1	1	1

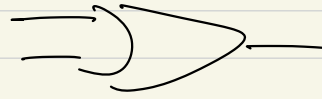
$$\text{AND} = \begin{bmatrix} 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

$$\begin{bmatrix} 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 1 \end{bmatrix}$$

$\underbrace{\hspace{10em}}_{|0\rangle} \quad \underbrace{\hspace{10em}}_{|0\rangle} \quad \underbrace{\hspace{10em}}_{|0\rangle} \quad \underbrace{\hspace{10em}}_{|1\rangle}$



* OR gate

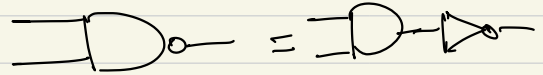


$$OR = \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 1 \end{bmatrix}$$

AB	0
00	0
01	1
10	1
11	1

$$\overline{AB}$$

* NAND gate



$$NAND = \begin{bmatrix} 0 & 0 & 0 & 1 \\ 1 & 1 & 1 & 0 \end{bmatrix}$$

00	1
01	1
10	1
11	0

$$\begin{bmatrix} 0 & 1 \\ 1 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 \end{bmatrix}$$

* NAND gate is a universal gate