

TDM Link Control

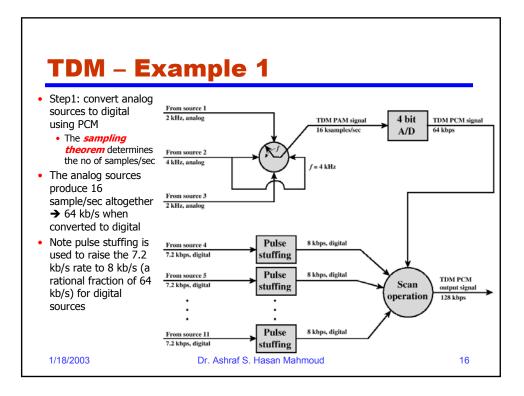
TDM frame:

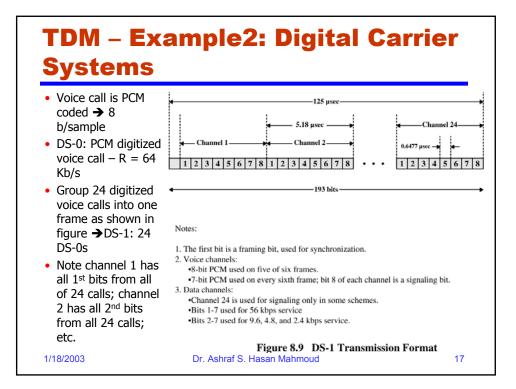
- No header and no error detection/control these are per connection procedures
- Frame synchronization is required to identify beginning and end of frame
 - Added-digit framing: One control bit is added to each start of frame all these bits from consecutive frame form an identifiable pattern (e.g. 1010101...)
 - These added bits for framing are inserted by system → control channel
 - Frame search mode: Rx-er parses incoming stream until it recognizes the pattern → then TDM frame is known
- Pulse stuffing:
 - Different sources may have separate/different clocks
 - · Source rates may not be related by a simple rational number
 - <u>Solution</u>: inflate lower source rates by inserting extra dummy bits or pulses to mach the locally generated clock speed

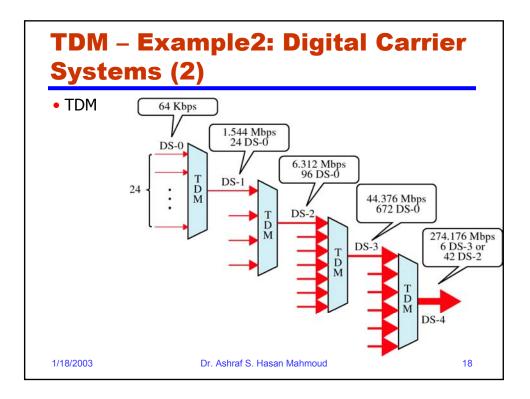
```
1/18/2003
```

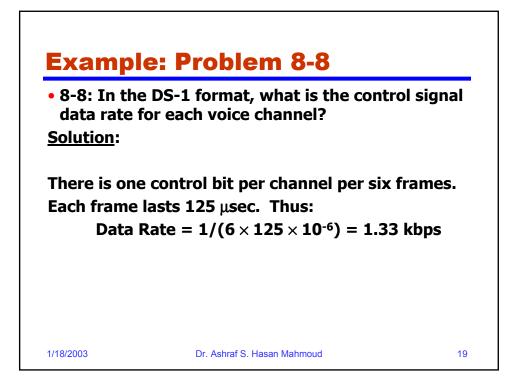
Dr. Ashraf S. Hasan Mahmoud

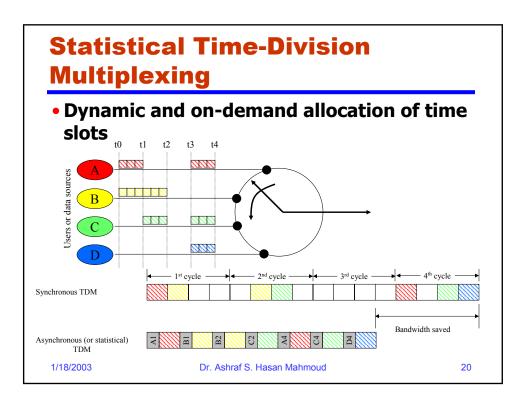
15

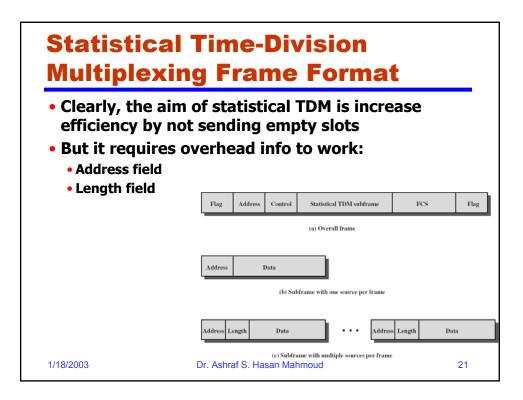


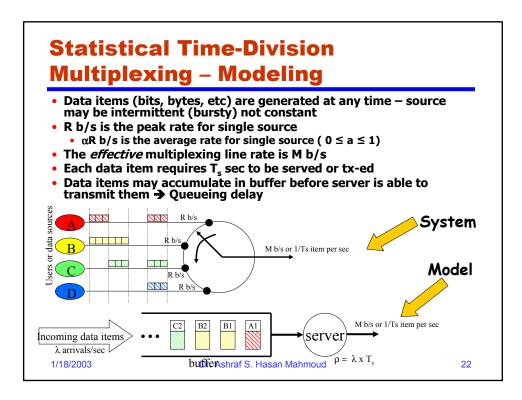


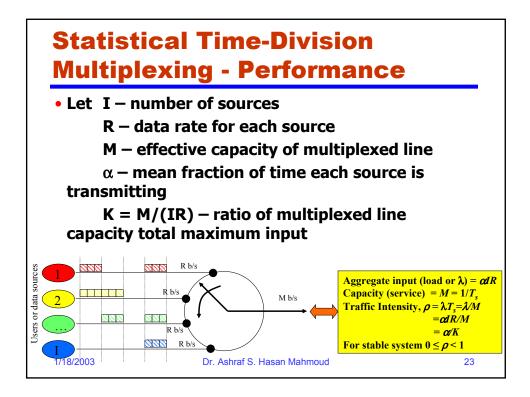


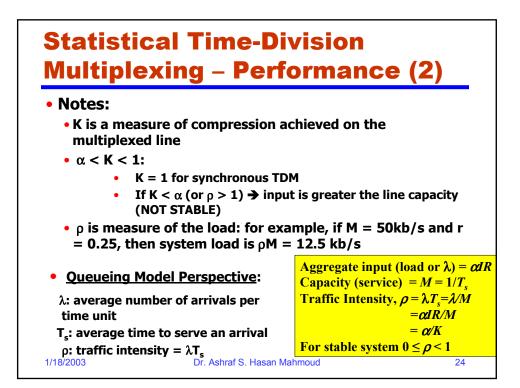


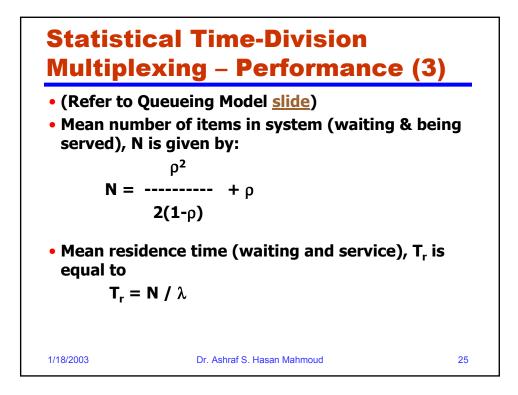


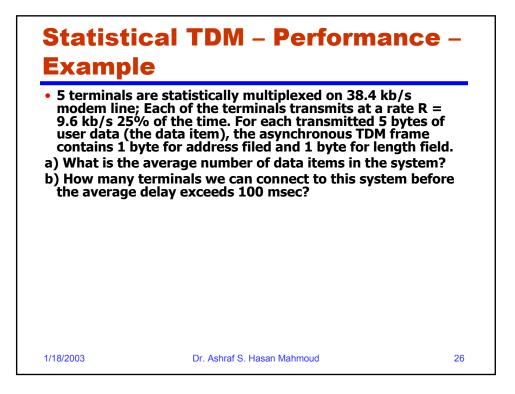




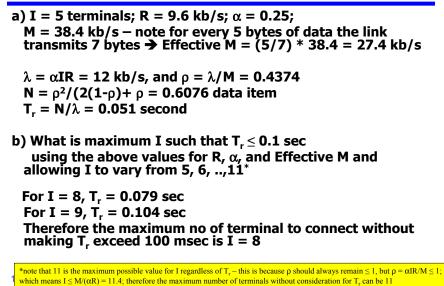


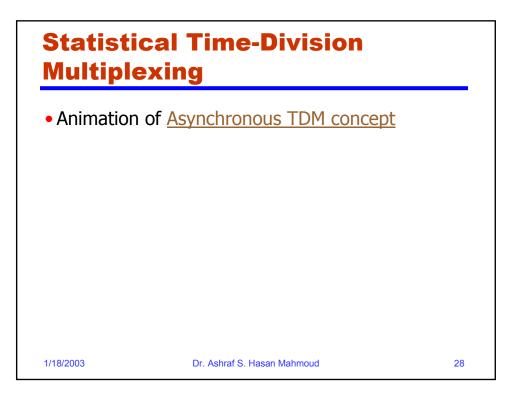






Statistical TDM – Performance – Example - Solution







 8-13: Ten 9.6 kb/s lines are to be multiplexed using TDM. Ignoring overhead bits, what is the total capacity required for synchronous TDM? Assuming that we wish to limit the average multiplexed line utilization to 0.8, and assuming that each line is busy 50% of the time, what is the capacity required for statistical TDM?

1/18/2003

Dr. Ashraf S. Hasan Mahmoud

29

Example: Problem 8-13 - solution

Synchronous TDM: M = IR; R = 9.6kb/s, I = 10 \Rightarrow M = ? M = 9600 bps × 10 = 96 kbps Statistical TDM: Remember that $\rho = \alpha IR/M$; $\rho = 0.8$, $\alpha = 0.5$, R = 9.6kb/s, I = 10 \Rightarrow M = ? M = 9600 bps × 10 × 0.5/0.8 = 60 kbps

1/18/2003

Dr. Ashraf S. Hasan Mahmoud

