## Parallel Algorithms

- Examples
- Concepts \& Definitions
- Analysis of Algorithms


## Lemma

- Any complete binary tree with $n$ leaves has
$\square$ internal nodes $=\mathrm{n}-1$ (i.e., $2 \mathrm{n}-1$ total nodes)
$\square$ height $=\log _{2} \mathrm{n}$
- Exercise: Prove it.


## Warming up

- Consider the BTIN (Binary Tree Interconnected Network) computational model. Suppose the tree has n leaves (and hence $2 \mathrm{n}-1$ processors).
- If we have n numbers stored at the leaves, how can we obtain the sum?
- How can we obtain the max or min?
- How can we propagate a number stored at the root to all leaves?


## Warming up

- Suppose we have $\mathrm{n}-1$ numbers stored at n-1 arbitrary leaves. How can we move these numbers to the n-1 internal nodes?
- If the leftmost $n / 2$ leaves have numbers, how can we move them the rightmost leaves?
- How many steps does each of the above computation require?


## Example 1.4

## Grouping in a shared-Memory PC

- Given a sequence of pairs $\left\{\left(\mathrm{x}_{1}, \mathrm{~d}_{1}\right), \ldots,\left(\mathrm{x}_{\mathrm{n}}, \mathrm{d}_{\mathrm{n}}\right)\right\}$ where $\mathrm{x}_{\mathrm{i}} \in\{0,1, . ., \mathrm{m}-1\}, \mathrm{m}<\mathrm{n}$, and $\mathrm{d}_{\mathrm{i}}$ is an arbitrary datum.
- By pigeonhole principle several $\mathrm{x}_{\mathrm{i}}$ will be repeated because $\mathrm{m}<\mathrm{n}$. Write a parallel algorithm to group these pairs according to the $\mathrm{x}_{\mathrm{i}}$ 's.


## Example 1.4

Grouping in a shared-Memory Model

- Sequential algorithm: for each step i, read x and insert it in the hash table.
- Time $=$ n steps

Memory $=\Theta(\mathrm{n})$


## Example 1.4: Parallel algorithm

Grouping in a shared-Memory Model

- Shared memory with n processors $\mathrm{P}_{1}, \mathrm{P}_{2}, . ., \mathrm{P}_{\mathrm{n}}$
- Memory $=\mathrm{m}(2 \mathrm{n}-1)$
- Think about m complete $\mathbf{B T} \mathrm{T}_{0}, \mathrm{~T}_{1}, \ldots, \mathrm{~T}_{\mathrm{m}-1}$ each with n leaves numbered $1,2, . ., \mathrm{n}$ which corresponds to $\mathrm{P}_{1}, . ., \mathrm{P}_{\mathrm{n}}$.
$0 \quad 1$



## Example 1.4: Parallel algorithm

## Grouping in a shared-Memory Model

- Phase 1: Each processor $\mathbf{P}_{\mathbf{i}}$ will read the pair $\left(\mathbf{x}_{\mathbf{i}}, \mathbf{d}_{\mathbf{i}}\right)$ and insert it in the leaf $\mathbf{i}$ that belongs to the tree $\mathbf{T}_{\mathbf{x}_{\mathbf{i}}}$
- Phase 2: Each processor $\mathbf{P}_{\mathbf{i}}$ will try to move the pair $\left(\mathbf{x}_{\mathbf{i}}, \mathbf{d}_{\mathbf{i}}\right)$ higher up in its tree until it can go no higher as follows:

0
1
m-1


# Example 1.4: Parallel algorithm Grouping in a shared-Memory Model 

## Shifting-up rule:

- If node u is free, then the pair in the right child (if any) takes precedence in moving to $u$ over the pair in the left child (if any).


## Example 1.4: Parallel algorithm <br> Analysis

- Since in shared memory parallel computer we have common program for all processors and they execute synchronously, then
- Phase 1: takes 1 step only
- Phase 2: it takes $\log _{2} n$ steps only because each tree has height $=\log _{2} \mathrm{n}$.
- Total $=\log _{2} \mathrm{n}+1 \quad$ steps
- Extra empty cells in the $\mathrm{m}(2 \mathrm{n}-1)$ memory can be released.


## Example: 1.5

Pipelining database in a BTIN model

- In a BTIN with n leaves (processors) containing n distinct records each of the form $(\mathrm{k}, \mathrm{d})$ where k is a key and d is a datum.
- Suppose that the root receives a query to retrieve the record whose key is K (if it exists)
- Write a parallel algorithm.


## Example: 1.5

Pipelining database in a BTIN model

- Sequential Algorithm: Use binary search algorithm after sorting the records according to the keys.
- Time $=\Theta(\mathrm{n} \log \mathrm{n})$

Example: 1.5: Parallel Algorithm

## Pipelining database in a BT'IN model

- The root sends the key K to its children which they send subsequently to their children and so on.
- Until it reaches the leaves where it is compared with the keys they stored there.
- The leaf that contains the key is going to send up the corresponding record to the root through its parent and grandparents. Other leaves will send null message.
- When a parent receives a record from one of its children then it will send the same record to its parent; otherwise it will send null.
- and so on ...


## Example: 1.5 Analysis

- This is called pipeline technique.
- All processor have the same program working asynchronously when they receive messages from parents or children.
- Time $=2 \log _{2} \mathrm{n}$ steps to send the key down the tree and receive back the record.


## Example: 1.5: Parallel Algorithm Pipelining database in a BT'IN model

- What if we make m queries K1, K2, ..., Km?
- Solution:
- Sends them sequentially one after another
- Total time $=2 \log _{2} n+m-1$


## Example 1.6 Prefix (Partial) Sums

- Given n numbers $\mathrm{x}_{0}, \mathrm{x}_{1}, \ldots, \mathrm{x}_{\mathrm{n}-1}$ where n is a power of 2 .
- Compute the partial sums for all $\mathrm{k}=0,1, . ., \mathrm{n}-1$

$$
\mathrm{S}_{\mathrm{k}}=\mathrm{x}_{0}+\mathrm{x}_{1}+\ldots+\mathrm{x}_{\mathrm{k}}
$$

## Example 1.6 Prefix (Partial) Sums

- Sequential Algorithm:

We need to make the unavoidable $\mathrm{n}-1$ additions.

## Example 1.6: Parallel Algorithm Prefix (Partial) Sums

- For $\mathrm{i}=0,1, \ldots \mathrm{n}-1$, let initially $S_{i}=\mathrm{x}_{\mathrm{i}}$
- Then for $\mathrm{j}=0, \ldots, \log _{2} \mathrm{n}-1$, let

$$
\mathrm{S}_{\mathrm{i}} \leftarrow \mathrm{~S}_{\mathrm{i}}+\mathrm{S}_{\mathrm{i}-2^{\wedge} \mathrm{j}} \quad \text { until } 2^{\wedge} \mathrm{j}=\mathrm{i}
$$

- This is can be done using the combinatorial Circuit model with $\mathrm{n}\left(\log _{2} \mathrm{n}+1\right)$ processors distributed over $\log _{2} \mathrm{n}+1$ columns and n rows.
- at each step we add the one that is at distance equal to twice the distance we use in the previous step.


## Example 1.6: Parallel Algorithm <br> Analysis

- The number of processors in the model is $\mathrm{n}\left(\log _{2} \mathrm{n}+1\right)$
- The number of columns is $\log _{2} \mathrm{n}+1$
- Each processor does at most one step (addition).
- The processors in any fixed column work in parallel.
- Time $=\log _{2} \mathrm{n}+1$ additions.


## Summary

- At the cost of increasing the computation power (the number of processors \& memory) we may be able to decrease the computation time drastically.


## !!Expensive computations!!

■ Is it worth it?

## Is it worth it?

- Parallel computation is done mainly to speed up computations, while requiring huge processing power.
- To produce Tera FLOPS, the number of CPUs in a massively parallel computer can reach 100s of 1000 s.
- This is OK if parallel computing is cheap enough comparing to the critical time reduction of the problem we are solving.
- In the close future, TFLOPS will be available by a single Intel Chip!!


## Is it worth it?

- This is indeed the case with many applications in medicine, business, and science that
- Process huge database,
- Deal with live streams from huge number of sources
$\square$ require huge number of iterations


## Analysis of Parallel Algorithms

- Complexity of algorithms is measured by $\square$ Time
$\bullet$ Parallel steps
- Number of CPUs


## Elementary Steps

1. Computational Steps: basic arithmetic or logical operations performed within a processor, e.g., comparisons, additions, swapping, ..etc

- Each takes a constant number of time units


## Elementary Steps

2. Routing Steps: steps used by the algorithm to move data from one processors to another via shared-memory or interconnections.

- It is different in shared-memory models than in the interconnection models.


## Routing in Shared-memory

- In shared-memory models: the interchange is done by accessing the common memory
- It is assumed that it can be done in
- constant time in the uniform model (unrealistic but easy to assume)
$-\mathrm{O}(\log \mathrm{M})$ in the non-uniform model with memory of size M.


## Routing in Interconnections

- In interconnection models: the routing step is measured by the number of links the message has to follow in order to reach the destination processor.
- That is, it is the distance of the shortest path between the source and the destination processors.


## Performance Measures

1. Running time
2. Speedup
3. Work
4. Cost
5. Cost optimality
6. Efficiency
7. Success ratio

## 1. Running Time

- It is measured by the number of elementary steps (computational and routings) the algorithm does from the time the first processor starts to work to the finishing time of last processor.
- Example:
$\square$ P1 performs 13 steps, then idle for 3 steps, then 4 steps more.
- P2 performs 11 steps continuously
$\square$ P3 performs 5 steps, then 11 steps more.
$\square$ Total $=20$ steps


## 1. Running Time

- The running time depends on the size of input and the number of processors which may depend on input.
- Some time we write $t_{\mathrm{p}}$ to denote the running time of a parallel algorithm that runs on a computer with p processors.


## 2. Speedup

- The speedup is defined by
$\mathrm{s}(1, \mathrm{p})=\mathrm{t}_{1} / \mathrm{t}_{\mathrm{p}}$
the best time known for seq. alg.
the time for par. alg. with p CPUs


## Speedup Folklore Theorem

- Theorem: The speedup $S(1, p) \leq p$

Proof:

- The parallel algorithm can be simulated by a sequential one in $\mathrm{t}_{\mathrm{p}} \times \mathrm{p}$ time (in the trivial way)
- Since $\mathbf{t}_{1}$ is an optimal time then $\mathbf{t}_{1} \leq \mathrm{t}_{\mathrm{p}} \times \mathrm{p}$
- That is $\mathrm{t}_{1} / \mathrm{t}_{\mathrm{p}} \leq \mathrm{p}$..... Right?
- Well not really!! True only for algorithms that can be simulated by sequential computers in $t_{p} \times p$ time


## Speedup Folklore Theorem

- Theorem: The speedup $S(1, p) \leq p$
- Conclusions:
- First,

$$
t_{\mathrm{p}} \geq \mathrm{t}_{1} / \mathrm{p}
$$

- This means that the running time of any parallel algorithm $t_{p}$ cannot be better than $t_{1} / p$
- Second, a good parallel algorithm is the one whose speedup is very close to the number of processors.


## Example 1.14: Adding in numbers

- Adding n numbers can be done in sequential computer by using $\mathrm{n}-1$ additions.
- In parallel computer, it can be done in $\mathrm{O}(\log \mathrm{n})$ steps by using BTIN model.
- The tree has $\mathrm{n} / \log \mathrm{n}$ leaves.
- Each leaf processor adds $\log \mathrm{n}$ numbers
- Each parent adds the number of its children and so on.
- The root will contain the result.


## Analysis

- Time to add numbers in each processor $=\mathbf{O}(\log n)$ steps
- Time to propagate $=\mathbf{O}(\log n)$ steps
- Speedup $=0(n / \log n)$



## Example 1.15:

## Searching in Shared Memory Model

- Given a number x and an array $\mathrm{A}[1 . . \mathrm{n}]$ containing n distinct numbers sorted increasingly, all stored in a memory.
- Write a parallel algorithm that searches for x in the list and returns its position.


## Example 1.15:

## Searching in Shared Memory Model

- Sequential algorithm: binary search algorithm can solve the problem in $\mathrm{O}(\log \mathrm{n})$ time.
- However, one can achieve $O(1)$ parallel time!
- In shared memory model with $n$ processors let each processor $\mathrm{P}_{\mathrm{i}}$ compares x with the cell $\mathrm{A}[\mathrm{i}]$
- Specify a location in the memory call it answer; initially answer $=0$.
- Any processor that finds x will write its index in the location answer.
- I.e. if answer $=\mathrm{i}$, then $\mathrm{P}_{\mathrm{i}}$ finds x in $\mathrm{A}[\mathrm{i}]$


## Example 1.15: <br> Analysis

- Surely the overall time is $\mathrm{O}(1)$ steps.
- Speedup $=\mathrm{O}(\log \mathrm{n}) \leq \mathrm{n}=$ \# of processors used
- Is it possible to use only $\mathrm{O}(\log \mathrm{n})$ processors to achieve the same performance?


## How about searching <br> in the BTIN model?

- Use the BTIN model with n leaves (and $2 \mathrm{n}-1$ total processors)
- The leaf processor $\mathrm{P}_{\mathrm{i}}$ holds $\mathrm{A}[\mathrm{i}]$
- The root will take the input x and propagates it to the leaves and the answer will return back to the root.
- Any parallel algorithm has to take at least $\Omega(\log \mathrm{n})$ to just traverse the links between the processors.
- Speedup $=\mathrm{O}(1)$ in the best which way smaller than $2 \mathrm{n}-1$ the number of processors.


## Having said that ...

- It appears that the speedup Theorem is not always true specially if the parallel computer has many different stream inputs which can't be simulated properly in a sequential computer.
- Counter Example: read 1.17.


## Slowdown Folklore Theorem

- Theorem: if a certain problem can be solved with $p$ processors in $\mathrm{t}_{\mathrm{p}}$ time and with q processors in $\mathrm{t}_{\mathrm{q}}$ time where $\mathrm{q}<\mathrm{p}$, then

$$
\mathrm{t}_{\mathrm{p}} \leq \mathrm{t}_{\mathrm{q}} \leq \mathrm{t}_{\mathrm{p}}+\mathrm{p} \mathrm{t}_{\mathrm{p}} / \mathrm{q}
$$

- That is when the number of CPUs decreases from p to $q$ then the running time can slowdown by a factor of $(1+p / q)$ in the worst case.
- Or when the number of CPUs increases from $q$ to $p$ then the running time can be reduced by a factor of $1 /(1+p / q)$ in the best case.


## Idea of Proof

- Suppose that you have a parallel algorithm that runs on a computer with p processors.
- To run the same algorithm on a computer with q processors you need to distribute the tasks that the p processors do (at most $\mathrm{p} \mathrm{t}_{\mathrm{p}}$ steps) into the q processors as evenly as possible.
- Thus, each processor will have to do at most $\mathrm{p} \mathrm{t}_{\mathrm{p}}$ /q steps .... And so $\mathrm{t}_{\mathrm{q}} \leq \mathrm{t}_{\mathrm{p}}+\mathrm{p} \mathrm{t}_{\mathrm{p}} / \mathrm{q}$.
- For detail See Page 18

$$
\begin{gathered}
\text { Slowdown Folklore Theorem } \\
\text { (Brent's Theorem) }
\end{gathered}
$$

- Notice that if $\mathrm{q}=1$, then

$$
1 \leq \mathrm{t}_{1} / \mathrm{t}_{\mathrm{p}}=\mathrm{S}(1, \mathrm{p}) \leq 1+\mathrm{p}
$$

- The Slowdown theorem is not always true specially when the distribution of input data or the communications impose overhead on the running time.
- See Example 1.19


## 3. Number of Processors

- Algorithms (with same running time and on same models) but with less number of processors are preferred (less expensive).
- Sometimes optimal times and speedups can be achieved with certain number of processors
- A minimum number of processors may be required to have successful computations
- Slowdown and speedup theorems show that number of processors is important.
- Certain computational models may not accommodate the required number of processors. (e.g., perfect squares or prime number)
- In combinatorial circuits each CPU is used at most once. That gives an upper bound on the time.


## 4. The Work

- The Work is defined to be the exact total number of elementary steps executed by all processors.
- Running time = maximum of elementary steps used by any processor
- Exercise: Think about the combinatorial circuit model.


## 5. The Cost

- The cost $C(n)$ is an upper bound on the total number of elementary steps used by all processors, and defined as

$$
\begin{aligned}
\mathrm{C}(\mathrm{n}) & =\mathrm{t}(\mathrm{n}) \times \mathrm{p}(\mathrm{n}), \text { where } \\
\mathrm{t}(\mathrm{n}) & =\text { the running time } \\
\mathrm{p}(\mathrm{n}) & =\text { number of processors }
\end{aligned}
$$

- Note: not all processors are necessarily active during the $\mathrm{t}(\mathrm{n})$ time units.
- In combinatorial circuit model: $C(n)=p(n)$ by definition.


## Example



### 5.1 Cost Optimality

- Notice that the cost is indeed the worst case running time needed to simulate a parallel algorithm on a sequential computer (if it can be done).
- For the following: we restricted ourself to "simulate-able" parallel algorithms only.

1. If $\Omega(\mathrm{f}(\mathrm{n}))$ number of steps are needed to solve a problem sequentially, and the cost of a parallel algorithm is $\mathrm{O}(\mathrm{f}(\mathrm{n}))$, then we say that the algorithm is asymptotically cost optimal.

### 5.1 Cost Optimality

- Recall Example 1.14 of Adding n numbers via BTIN. We used $\mathrm{p}=\mathrm{O}(\mathrm{n} / \log \mathrm{n})$ processors to achieve $O(\log n)$ running time.
- So the cost $=\mathrm{O}(\mathrm{n})$.
- But adding any n numbers need $\Omega(\mathrm{n})$ sequential steps. Thus the cost is optimal.
- Notice: if we use BTIN with $n$ leaves, the cost is $\mathrm{O}(\mathrm{n} \log \mathrm{n})$ which is not optimal.


## r'his means that ..

- If $\Omega(f(n))$ is a lower bound on the required number of steps to solve a problem of size $n$, then $\Omega(f(n) / p)$ is a lower bound on the running time of parallel algorithm with p processors.

This follows from the speedup theorem which says that the reduction in the running time is by at most a factor of $1 / p$.

- Example: Any parallel algorithm that uses $n$ processors needs $\Omega(\log n)$ steps to sort $n$ numbers, because sequential sorting needs $\Omega(n$ $\log n)$ steps.


### 5.1 Cost Optimality

2. The cost of a parallel algorithm is not optimal if an equivalent sequential algorithm exists whose worst case running time is better than the cost.

- Recall Example 1.4 of grouping n pairs into m groups. We used $n$ processors in a shared memory model to solve the problem in $\mathrm{O}(\log \mathrm{n})$ time.
- The cost is $\mathrm{O}(\mathrm{n} \log \mathrm{n})$ steps which is not optimal because the sequential algorithm uses $\mathrm{O}(\mathrm{n})$ steps.


### 5.1 Cost Optimality

3. Unknown cost optimality is possible when we have parallel algorithm whose cost matches the best known sequential running time but we don't know if the sequential running time is optimal.

- Example: Matrix multiplication requires $\Omega\left(\mathrm{n}^{\wedge} 2\right)$ steps. The best known sequential algorithm takes $\Omega\left(\mathrm{n}^{\wedge} \mathrm{c}\right)$ where $2<\mathrm{c}<2.38$. We don't know if it is optimal though.


### 5.2 Efficiency

- The efficiency of a parallel algorithm is defined by

$$
E(1, p)=t_{1} /\left(p t_{p}\right), \text { where }
$$

- $\mathrm{t}_{1}$ is the running time of the best known sequential algorithm,
- $t_{\mathrm{p}}$ is the running time of the parallel algorithm that runs on a computer with p processors.


### 5.2 Efficiency

- If the parallel algorithm is within our restriction, then $\mathrm{E}(1, \mathrm{p}) \leq 1$.
- If $\mathrm{E}(1, \mathrm{p})<1$, the parallel algorithm is not cost optimal .... NOT GOOD
- If $\mathrm{E}(1, \mathrm{p})=1$ and $\mathrm{t}_{1}$ is optimal, then the parallel algorithm is cost optimal .... GOOD
- If $E(1, p)>1$, then the simulated sequential algorithm (if doable!) is faster than the parallel algorithm. ... IDEAL
- Read Example 1.26


## Summary

- It is unfair to compare the running time of a parallel algorithm $t_{\mathrm{p}}$ to the running time of the best known sequential algorithm $t_{1}$.
- We should compare the cost $=\mathrm{p} \mathrm{t}_{\mathrm{p}}$ to $\mathrm{t}_{1}$.


## 6. Success Ratio

- Consider algorithms that solve problems correctly with certain probabilities.
- Let $\operatorname{Pr}(\mathrm{p})=$ the probability of success that a parallel algorithm solves correctly a given problem.
- Let $\operatorname{Pr}(1)=$ the probability of success that a sequential algorithm solves correctly the same given problem.
- The success ratio is defined by

$$
\operatorname{Sr}(1, \mathrm{p})=\operatorname{Pr}(\mathrm{p}) / \operatorname{Pr}(1)
$$

## 6. Success Ratio

- The success ratio is defined by

$$
\operatorname{sr}(1, \mathrm{p})=\operatorname{Pr}(\mathrm{p}) / \operatorname{Pr}(1)
$$

- The scaled success ratio is

$$
\operatorname{ssr}(1, p)=\operatorname{Pr}(\mathrm{p}) /(\mathrm{p} \times \operatorname{Pr}(1))
$$

- Usually: $\operatorname{sr}(1, \mathrm{p}) \leq \mathrm{p}$ and $\operatorname{ssr}(1, \mathrm{p}) \leq 1$

