KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

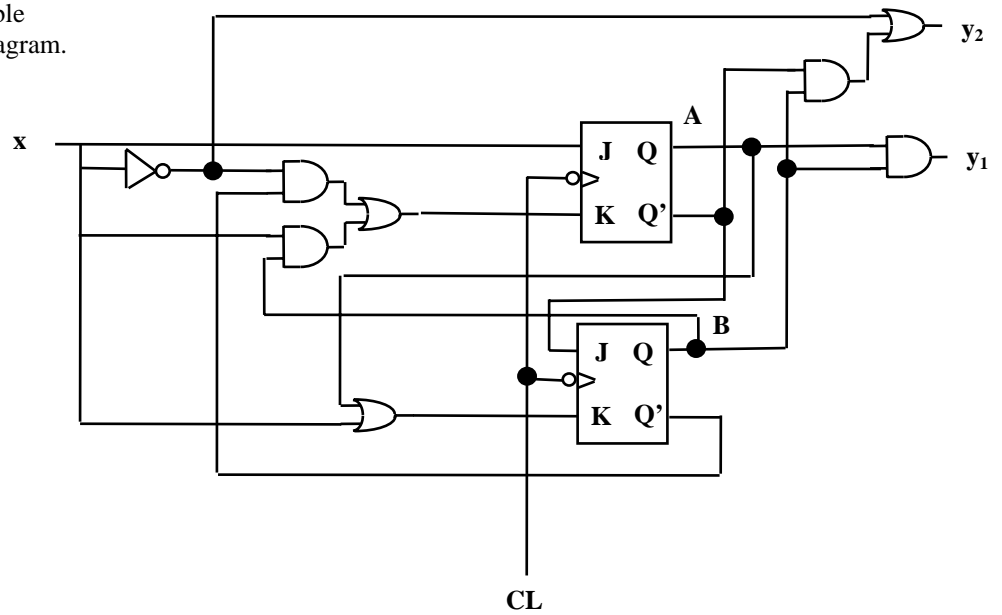| **EE200 Digital Logic Circuit Design**	**Quiz # 6**	**5 January 2008**

Student's Name: **I.D.#:**

A sequential circuit has two JK flip-flops, one input x and two outputs y1 and y2. The logic diagram of the circuit is shown. Analyse the circuit and obtain the following:

1. The flip-flop input functions
2. The output functions y1 and y2
3. The state table
4. The state diagram.

Instructor : Dr. M.M. Dawoud   *(071)*

Score :	*/10*