KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design	**Quiz # 4**	**12 November 2007**

Student's Name: **I.D.#:**

Design a combinational circuit that will accept a three bit binary number (x, y, z) and generates the 2's complement of the input binary number (A, B, C). Implement the logic circuit using only NOR gates

Instructor : Dr. M.M. Dawoud   *(071)*

Score :   */10*