

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design	Quiz #7 (2/6/2007)	Section:
Student's Name :		I.D.# :

Design a **SYNCHRONOUS COUNTER** using T flip flops whose count cycle consists of the binary equivalent of the odd numbers 1, 3, 5, 7 before resetting to 1.

Do not write below this line

Instructor : Dr. M.M. Dawoud

(062)

Score :	/10
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