

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design	Quiz #8 (2/1/2006)	Section: 03
Student's Name :		I.D.# :

Design a modulus-6 counter that goes through the count sequence (0-1-2-3-4-5) when an input $x=0$. When $x=1$ the counter becomes a down counter with the count sequence (5-4-3-2-1-0). Consider the unused states as don't care conditions and use T flip-flops.

Do not write below this line
