

Computer Aided Design of Digital Systems - COE572

Homework II.

Date: Fall 2003.

Question 1.

Assume that it is difficult to find benchmarks to verify the performance of a partitioning heuristic that you have designed. The only way to handle this is to generate suitable graphs whose minimal cost is known. Suggest ways to generate/design such graphs. (Note that random graphs will not work).

Question 2.

Solve Exercise 2.5 from the text.

Question 3.

For the circuit given in Example 2.6 (page 76), solve the problem using Simulated Annealing. Experiment with different values of parameters and plot the results.