Name: **Id#**

ICS 233, Term 072

Computer Architecture & Assembly Language

Quiz# 5

Date: Monday, April 21, 2008

Q.1.   Given that **Multiplicand=1110** and **Multiplier=1011**. Using the **refined signed multiplication** hardware, show the **signed** multiplication of **Multiplicand** by **Multiplier**. The result of the multiplication should be an 8 bit **signed** number in HI and LO registers. Show the steps of your work by filling the given table.

Q.2.   Given that **Dividend=1110** and **Divisor=0110**. Using the **refined unsigned division** hardware, show the **unsigned** division of **Dividend** by **Divisor**. The result of division should be stored in the Remainder and Quotient registers. Show the steps of your work by filling the given table.