

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering
EE200 Digital Logic Circuit Design

6 October 2010	Quiz #1 (6/10/2010)	Section: 03
Student's Name :		I.D.# :

1. The following numbers are in signed-2's complement form. $X = 111001010011$ and $Y = 000110101110$. Express X and Y as signed decimal numbers. Perform The binary sum $X+Y$ and express the result in decimal.
 2. Express the decimal number 240.75 in 2421 and in ASCII codes.
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Do not write below this line

Instructor : Dr. M.M. Dawoud

(101)

Score :	/10
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