

**KING FAHD UNIVERSITY OF PETROLEUM & MINERALS**  
**Department of Electrical Engineering**  
**EE200 Digital Logic Circuit Design**

<b>6 October 2010</b>	<b>Quiz #1 (6/10/2010)</b>	<b>Section: 01</b>
<b>Student's Name :</b>		<b>I.D.# :</b>

1. The following numbers are in signed-2's complement form.  $X = 000110101101$  and  $Y = 111001010010$ . Express  $X$  and  $Y$  as signed decimal numbers. Perform The binary sum  $X+Y$  and express the result in decimal.
  2. Express the decimal number 240.75 in BCD, and in Excess-3.
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Do not write below this line

*Instructor : Dr. M.M. Dawoud*

(101)

<b>Score :</b>	<b>/10</b>
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