## EXPERIMENT \# 4

## CURRENT AND VOLTAGE DIVIDER RULES

## OBJECTIVE:

To experimentally verify the current divider rule (CDR) for parallel circuits and the voltage divider rule for series circuits.

## Pre-Lab Assignment

For the circuit shown in Figure 2a and Figure 2b, calculate:
1- The unknown voltages and currents shown. Vs=10V.
2- The equivalent resistance seen by $\mathrm{V}_{\mathrm{s}}$.
Hint: Read through this experiment.

APPARATUS: DC Power Supply
Digital Multimeter
Carbon Resistors: $100 \Omega, 250 \Omega, 220 \Omega$ and $330 \Omega$.

## THEORY:

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Series Connections:
By definition, a series connection between any two electrical elements is such that the same current passes through both elements. For example element e1, e2, and e3 in Figure 1 are in series since the same current I1 passes through them.

Figure 1


## Parallel Connections:

By definition, a parallel connection between any two electrical elements is such that the voltage is the same across both the elements. For example, elements e4 and e6 in Figure 1 are in parallel, since the same voltage $\left(\mathrm{V}_{4}=\mathrm{V}_{6}\right)$ is across both of them. Also, element e5 and e7 are in parallel for a similar reason.

## Voltage Divider Rule (VDR) :

For a discussion of VDR, refer to your textbook. As an example of VDR, in Figure 1, if e1 =R1, e2 = R2, and e3 $=$ R3, then:

$$
\begin{align*}
& V_{1}=\frac{R_{1}}{R_{1}+R_{2}+R_{3}} V_{s}  \tag{1}\\
& V_{2}=\frac{R_{2}}{R_{1}+R_{2}+R_{3}} V_{s} \tag{2}
\end{align*}
$$

## Current Divider Rule (CDR):

For a discussion of CDR, refer to your textbook. As an example of CDR in Figure 1, If e4=R4 and e6 = R6, then:

$$
\begin{equation*}
I_{4}=\frac{R_{6}}{R_{4}+R_{6}} I_{1} \tag{3}
\end{equation*}
$$

For I5 and I7, CDR can be applied as

$$
\begin{align*}
& I_{5}=\frac{R_{7}}{R_{5}+R_{7}}\left(I_{4}+I_{6}\right)  \tag{4}\\
& I_{7}=\frac{R_{5}}{R_{5}+R_{7}}\left(I_{4}+I_{6}\right)
\end{align*}
$$

This is valid, since I4 + I6 is the total current passing though the parallel combination in this case.

## Procedure:

1- Check the values of resistors used in the circuit of Figure 2, using the multimeter. Record the values in Table 1.


Figure2

1. Connect the circuit of Figure 2 a and adjust the supply voltage Vs to 10 V , using the DC voltmeter.
2. Measure the entire unknown voltages and currents shown. Record their values in Table 2.
3. Measure $\mathrm{R}_{\mathrm{eq}}$ using an Ohmmeter and record its values in Table 2.
4. Connect the circuit of Figure 2 b and adjust the supply voltage Vs to 10 V , using the DC voltmeter.
5. Measure the entire unknown voltages and currents shown. Record their values in Table3 (recall that when measuring current by an ammeter, the ammeter should be placed in series with the element in which the current passes. Keep this fact in mind when measuring I5.
6. Measure $\mathrm{R}_{\mathrm{eq}}$. and record its value in Table 3 .

## Report:

1- Compare the theoretical and experimental values of voltages and currents and the equivalent resistance of both circuits. Calculate the percent errors.
2- Give reasons for any discrepancies.

## Questions:

Referring to the circuit of Figure 2a:
1- Are R4 and R6 in parallel or in series?
2- Are R3 and R4 in parallel or in series?
3- Are Vs and R6 in series or in parallel?
4- Is VDR applicable for applicable for R3 and R4?
5- Is CDR applicable for R4 and R6?
6- Is the parallel combination of R4 and R6 in series or in parallel with R2?
Justify your answer in each case by referring to the experimental results.
TABLE 1

## Resistor Values:

| Resistor | R2 | R3 | R4 | R6 |
| :--- | :---: | :---: | :---: | :---: |
| Nominal Value ( Ohm ) | 100 | 220 | 150 | 330 |
| Ohmmeter reading |  |  |  |  |

TABLE 2
Circuit of Figure 2a:

| Unknown | $\mathrm{I}_{1}$ | $\mathrm{I}_{2}$ | $\mathrm{I}_{3}$ | $\mathrm{I}_{4}$ | $\mathrm{I}_{5}$ | $\mathrm{I}_{6}$ | $\mathrm{~V}_{2}$ | $\mathrm{~V}_{3}$ | $\mathrm{~V}_{4}$ | $\mathrm{~V}_{5}$ | $\mathrm{~V}_{6}$ | $\mathrm{R}_{\mathrm{eq}}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Theory |  |  |  |  |  |  |  |  |  |  |  |  |
| Experiment |  |  |  |  |  |  |  |  |  |  |  |  |
| \% Error |  |  |  |  |  |  |  |  |  |  |  |  |

TABLE 3
Circuit of Figure 2b

| Unknown | $\mathrm{I}_{1}$ | $\mathrm{I}_{2}$ | $\mathrm{I}_{3}$ | $\mathrm{I}_{4}$ | $\mathrm{I}_{5}$ | $\mathrm{I}_{6}$ | $\mathrm{~V}_{2}$ | $\mathrm{~V}_{3}$ | $\mathrm{~V}_{4}$ | $\mathrm{~V}_{5}$ | $\mathrm{~V}_{6}$ | $\mathrm{R}_{\mathrm{eq}}$ |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Theory |  |  |  |  |  |  |  |  |  |  |  |  |
| Experiment |  |  |  |  |  |  |  |  |  |  |  |  |
| \% Error |  |  |  |  |  |  |  |  |  |  |  |  |

