

*King Fahd University of Petroleum and Minerals*  
*College of Computer Science and Engineering*  
*Computer Engineering Department*

**COE 202: Digital Logic Design (3-0-3)**  
**Term 141 (Fall 2014-2015)**  
**Major Exam 1**  
**Saturday October 18, 2014**

**Time: 90 minutes, Total Pages: 8**

Name: KEY ID: \_\_\_\_\_ Section: \_\_\_\_\_

**Notes:**

- Do not open the exam book until instructed
- **No Calculators are allowed** (*basic, advanced, cell phones, etc.*)
- Answer all questions
- All steps must be shown
- Any assumptions made must be clearly stated

Question	Maximum Points	Your Points
1	17	
2	16	
3	12	
4	15	
Total	60	

**Question 1.****(17 points)**

- (I) Without converting to decimal, express the binary number  $(111.0101)_2$  in **Octal** and **Hexadecimal**. **(4 points)**

$$(111.010_100)_2 = (7.24)_8.$$

$$(0111.0101)_2 = (7.5)_{16}.$$

- (II) Express the decimal number 129.33 in **Binary** and **BCD** (with 4-bit fraction accuracy). **(6 points)**

$$129 = 128 + 1 = 2^7 + 2^0 = 10000001.$$

$$0.33 \times 2 = 0.66 \Rightarrow 0$$

$$0.66 \times 2 = 1.32 \Rightarrow 1$$

$$0.32 \times 2 = 0.64 \Rightarrow 0$$

$$0.64 \times 2 = 1.28 \Rightarrow 1$$

$$129.33 = (1000\ 0001.0101)_2 = (0001\ 0010\ 1001.0011\ 0011)_{\text{BCD}}$$

- (III) Adding an **even parity** bit as the MSB, the Binary code 1001101 becomes 01001101. **(1 point)**

- (IV) Perform the following arithmetic operations in the given bases. (Show your work) **(6 points)**

Binary Multiplication	Binary Subtraction	Hexadecimal Addition
$\begin{array}{r} 1011 \\ \times \underline{101} \\ \hline 1011 \\ 00000 \\ \underline{101100} \\ 110111 \end{array}$	$\begin{array}{r} 100.10 \\ - \underline{11.01} \\ \hline 001.01 \end{array}$	$\begin{array}{r} 37A \\ + \underline{93} \\ \hline 40D \end{array}$

**(16 points)****Question 2.**

Use Boolean algebra to solve the following questions. Show clearly all your steps.

(I) Simplify each the following Boolean functions to the specified number of literals:

a.  $F1 = x y z + \bar{x} y + \bar{x} \bar{y}$  **(3 literals)**

**(3 points)**

$$= xyz + x'(y + y')$$

$$= xyz + x'$$

$$= x' + yz$$

b.  $F2 = \bar{x} \bar{y} \bar{z} + \bar{x} y \bar{z} + \bar{x} y z + x \bar{y} \bar{z}$  **(4 literals)**

**(4 points)**

$$= y'z'(x' + x) + x'y(z' + z)$$

$$= y'z' + x'y$$

c.  $F3 = w x + w x y + \bar{w} y z + \bar{w} \bar{y} z + \bar{w} x y \bar{z}$  **(6 literals)**

**(5 points)**

$$= (wx + wxy) + (w'yz + w'y'z) + w'xyz'$$

$$= wx + w'z + w'xyz'$$

$$= wx + w'(z + xyz')$$

$$= wx + w'(z + xy)$$

$$= wx + w'z + w'xy$$

$$= w'z + x(w + w'y)$$

$$= w'z + x(w + y)$$

$$= w'z + wx + xy$$

(II) **Without simplification**, write out the **complement** and **dual** forms of the following expression:

$$(x + \bar{y} \bar{z})(w \bar{x} z + \bar{w} y \bar{z})$$

**(4 points)**

The **complement** of the function is:

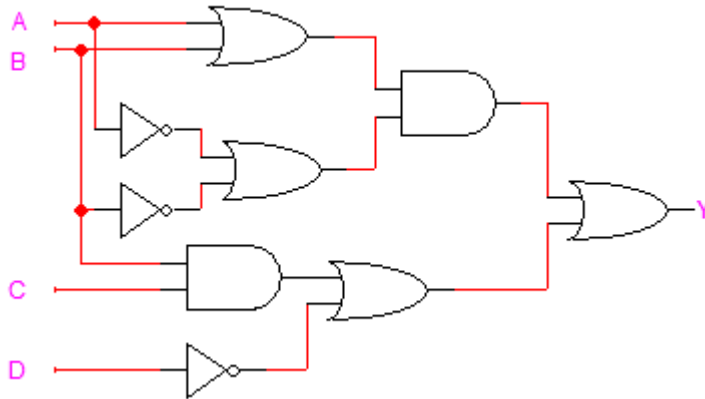
$$F' = x' \cdot (y + z) + (w' + x + z') \cdot (w + y' + z)$$

The **dual** of the function is:

$$x \cdot (y' + z') + (w + x' + z) \cdot (w' + y + z')$$

**Question 3.****(12 points)**

(I) Express the Boolean function Y of the following given circuit without simplification:

**(3 points)**

$$Y = (A + B)(\bar{A} + \bar{B}) + BC + \bar{D}$$

(II) Given the Boolean function  $F(X, Y, Z) = (X + Y)(\bar{X} + Z)(\bar{Y} + \bar{Z})$ :**(6 points)**a. Express F as a **product-of-maxterms**,  $F = \prod M$ .b. Find the **algebraic sum-of-minterms** expression for F.

$$\bar{F} = \bar{X}\bar{Y} + X\bar{Z} + YZ = \sum m(0, 1, 3, 4, 6, 7)$$

$$F = \prod M(0, 1, 3, 4, 6, 7)$$

$$b. F = \sum m(2, 5)$$

$$F = \bar{X}Y\bar{Z} + X\bar{Y}Z$$

(III) Given that  $F(A, B, C) = \sum m(0, 2, 5, 6)$  and  $G(A, B, C) = \prod M(1, 2, 5, 7)$ , express the function  $F \cdot \bar{G}$  as a **sum-of-minterms**. **(3 points)**

$$\bar{G} = \sum m(1, 2, 5, 7)$$

$$F \cdot \bar{G} = \sum m(2, 5)$$

**Question 4.**

**(15 points)**

(I) Circle the correct word in the following statements: **(3 Points)**

- a. It is desirable to have a low noise margin  $NM_L$  as ( **large** - small ) as possible
- b. It is desirable to have a high noise margin  $NM_H$  as ( **large** - small ) as possible
- c. It is desirable to have  $V_{IH}$  as ( large - **small** ) as possible
- d. It is desirable to have  $V_{IL}$  as ( **large** - small ) as possible
- e. It is desirable to have  $V_{OH}$  as ( **large** - small ) as possible
- f. It is desirable to have  $V_{OL}$  as ( large - **small** ) as possible

(II) Fill in the Truth Table for each of the following three circuits. Indicate whether the circuit operates properly or not. If circuit operation is improper (invalid) state the reason for that. Inputs A, and B are independent of one another and may assume any possible binary values.

**(6 Points)**

Circuit	If circuit operation is valid, what is the output function?	If circuit operation is invalid, state why.															
	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>F</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>X</td> </tr> <tr> <td>1</td> <td>0</td> <td>X</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	B	F	0	0	0	0	1	X	1	0	X	1	1	0	<p>The output has a short circuit at 2 conditions <math>AB= 01</math> and <math>10</math></p>
A	B	F															
0	0	0															
0	1	X															
1	0	X															
1	1	0															
	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>X</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	B	X	0	0	0	0	1	1	1	0	0	1	1	0	<p>✓</p>
A	B	X															
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	<table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>Y</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>Hi-Z</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table>	A	B	Y	0	0	Hi-Z	0	1	1	1	0	0	1	1	0	<p>The output has a short circuit at <math>AB= 11</math> but the outputs are the same (0) in this case, i.e. no conflict.</p>
A	B	Y															
0	0	Hi-Z															
0	1	1															
1	0	0															
1	1	0															

(III) The shown table gives propagation delays of some basic gates. For the circuit shown below, answer the following:

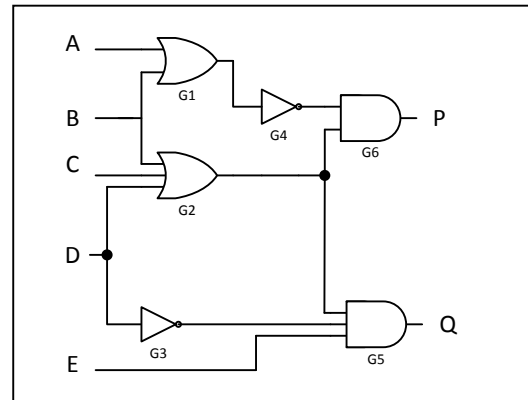
- d. Identify the critical path of the circuit (path with longest propagation delay). What is its associated delay value?

**(3 Points)**

Gate	Delay
Inverter	1 ns
2-Input AND	2 ns
2-Input OR	3 ns
3-Input AND	3 ns
3-Input OR	4 ns

Critical Path is: G2-G5

Delay of Critical Path = 4 + 3 = 7ns



- e. What is the maximum frequency at which the circuit may be operated? **(1 Point)**

$$F_{\max} = 1/(7 \times 10^{-9}) \approx 142.8 \text{ MHz}$$

- f. The gate which drives the largest load is gate   **G2**   **(1 Point)**

- g. Name a gate that has the highest fanin in the circuit (   **G2, G5**   ), the fanin of this gate is   **3**   **(1 Point)**