COE 205, Term 082

## Computer Organization \& Assembly Programming

## Quiz\# 2

Date: Monday, March 23, 2009

Q1. Consider an 8-bit register that has the binary number 10010110. Determine the decimal value of the number if it represents:
(i) An unsigned number.
(ii) A signed number in 2's complement representation.

Q2. Perform the following arithmetic operations assuming that numbers are represented using 8bit 2's complement representation. Indicate in your answer when an overflow occurs.
(i) $\mathrm{CB}+\mathrm{CA}$
(ii) $68-\mathrm{E} 0$

Q3. Fill the blanks in the following questions:
(i) Assuming unsigned number representation, the hexadecimal number (8F) $\mathbf{1 6}_{\mathbf{6}}$ represents the decimal number $\qquad$ .
(ii) The decimal number $\mathbf{1 2 0}$ is represented in binary as $\qquad$ .
(iii) The binary number $\mathbf{1 1 0 0 0 1 1 1}$ represents character $\qquad$ and uses an $\qquad$ parity bit. Note that the ASCII code of character $\mathbf{A}$ is 41 H and that of character $\mathbf{a}$ is 61 H .
(iv) Assuming 8-bit 2`s complement representation, the smallest (negative) number is
$\qquad$ in binary and $\qquad$ in decimal and the largest (positive) number is $\qquad$ in binary and $\qquad$ in decimal.

