## COE 205, Term 003

## Computer Organization \& Assembly Programming

## Quiz\# 2

Date: Tuesday, July 10
Suppose that you have the following initial content of the Intel 8086 registers:
$A X=F A B 1 H$
BX=FFFFH
CX=FFFAH
IP $=011 \mathrm{AH}$
SI $=0003 \mathrm{H}$
DI $=0005 \mathrm{H}$
DS =4AEBH
CS $=9000 \mathrm{H}$
(1) Suppose that the following data segment is allocated in the segment given in the DS register with an offset of 0 . Show the content of the allocated memory, and determine the physical address of next instruction to be fetched from memory. Note that the ASCII code of character ${ }^{`} 0 `$ is 30 H .

| I | $D B$ | -100, `12` | Address (Hex) | Memory Content |
| :---: | :---: | :---: | :---: | :---: |
| (hex) |  |  | 000A |  |
|  | DW | -100, `12` | 000B |  |
|  | DD | -1 | 000C |  |
| $L$ | EQU | 255 | 000D |  |
| J | DB | L-25 | O00E |  |
|  | DW | offset I+2 | 000F |  |
| K | DB | 2, 2 dup(5, 3 dup(-10)) | 0010 |  |
| M | DB | 'Q\#2', '\$' | 0011 |  |
|  |  |  | 0012 |  |
| Address (Hex) |  | Memory Content (hex) | 0013 |  |
| 0000 |  |  | 0014 |  |
| 0001 |  |  | 0015 |  |
| 0002 |  |  | 0016 |  |
| 0003 |  |  | 0017 |  |
| 0004 |  |  | 0018 |  |
| 0005 |  |  | 0019 |  |
| 0006 |  |  | 001A |  |
| 0007 |  |  | 001B |  |
| 0008 |  |  | 001C |  |
| 0009 |  |  | 001D |  |

Physical address of next instruction to be fetched $=$
(2) Show the content of the registers and memory locations modified after the execution of each of the following instructions. Use the initial content of the registers and memory locations as initial values for the subsequent instructions. Furthermore,
specify the addressing modes of the source and destination operands in each instruction.
a. SUB CL, J-1
b. MOV WORD PTR J-1, offset I+2
c. ADD [DI-3], AL
d. MOV WORD PTR [BX+SI +12$],-2$
(3) Give on a separate line the characters displayed on the screen by each of the following code fragments.
a. MOV DX, offset M

MOV AH, 09H
INT 21H
b. MOV DX, offset M+2

MOV AH, 09H
INT 21H
c. MOV DL, M

MOV AH, 02H
INT 21H

