

Name:

Id#

**COE 205, Term 023**  
**Computer Organization & Assembly Programming**

**Quiz# 1**

Date: Tuesday, July 8, 2003

**Q1.** Fill the blank in each of the following questions:

- i. The \_\_\_\_\_ register holds the address of the next instruction to be fetched from memory.
- ii. The \_\_\_\_\_ register holds the fetched instruction to be executed.
- iii. The \_\_\_\_\_ register is connected to the address bus in the CPU memory interface.
- iv. The \_\_\_\_\_ register is connected to the data bus in the CPU memory interface.
- v. The Instruction Set Architecture (ISA) of a computer consists of \_\_\_\_\_, \_\_\_\_\_, and \_\_\_\_\_.
- vi. The size of the address bus in the 8086 processor is \_\_\_\_\_ bits while in the Pentium Processor it is \_\_\_\_\_ bits.
- vii. The size of the data bus in the 8086 processor is \_\_\_\_\_ bits while in the Pentium Processor it is \_\_\_\_\_ bits.
- viii. Reading an instruction from Memory is performed in the \_\_\_\_\_ phase.
- ix. Reading operands from Memory is performed in the \_\_\_\_\_ phase.
- x. Incrementing the Instruction Pointer is performed in the \_\_\_\_\_ phase.
- xi. Decoding an instruction is performed in the \_\_\_\_\_ phase.