

Short Quiz - 1
EE-390 1
Set A

Name: _____

ID No: _____

(1) If an 8088 CPU register have DS='EFE1_H', SI='00EC_H', DI='00ED_H', Find the following answers using the given table (if needed):

(a) the P.A. of the last (highest) storage location: ______H

(b) the logical address of the destination data: ______H

(c) the value of the source data-word: ______H

(d) the word of last question in 'c' is aligned or misaligned:

______H

| Physical Address(PA) | Memory Contents |
|----------------------|------------------|
| 00000 _H | 1 5 _H |
| 00001 _H | 6 A _H |
| 00002 _H | 4 7 _H |
| 00003 _H | E A _H |
| 00004 _H | C 7 _H |
| . | |
| . | |
| EFEFB _H | 0 D _H |
| EFEFC _H | 1 A _H |
| EFEFD _H | 9 C _H |
| EFEFE _H | 8 4 _H |
| EFEFF _H | B 6 _H |

(2) Maximum 8-bit storage locations supported by the 16-bit address bus of a 8088 system is : _____

(3) Signed data of "7F_H" is a positive or negative: _____.