

Name: *Key*

Quiz # 2

ID#

- Design a limiter circuit using only diodes and $1k\Omega$ resistor to provide an output signal limited to the range, $-1.4V \leq V_o \leq 2.1V$. Use the constant voltage drop model ($V_{D0}=0.7V$).
- Plot output signal $V_o(t)$ if the input signal $V_i(t)$ is a sinusoidal signal with peak voltage $1.6V$.

⊗ Limiter Circuits

