

Design Project EE201-06 (032)

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An attenuator is an interface circuit that reduces the voltage level without changing the output resistance.

- (a) By specifying R_s and R_p of the interface circuit shown in the figure below, design an attenuator that will meet the following requirements:

$$V_o/V_g = 0.125, \quad R_{eq} = R_{Th} = R_g = 100\Omega$$

- (b) Using the interface designed in part (a), calculate the current through a load of $R_L=50\Omega$ when $V_g = 12V$.

