

بِسْمِ اللَّهِ الرَّحْمَنِ الرَّحِيمِ

King Fahd University of Petroleum and Minerals  
Department of Electrical Engineering

Semester I 2005/2006 EE455 Analog Communication Electronics Final Test

**ATTEMPT ALL QUESTIONS  
TIME ALLOWED THREE HOURS**

- Q1. Consider the circuit shown in Fig. 1 and answer the following questions:
- Assign trap for the 41.25 MHz sound carrier.
  - Assign trap for the 47.25 MHz, adjacent sound carrier.
  - Assign a chroma takeoff. **Justify your answer.**
  - Select design values for the resistors  $R_1$ ,  $R_2$  and  $R_3$  so that  $g_m$  of transistor  $Q_1$  will be 40mA/V.  $\beta = 100$ .
  - What is the function of the capacitor  $C_1$ ?
  - Assign a sound take off. Draw a possible circuit for the sound detector.
  - What is the function of the capacitor  $C_2$ ?
- Q2. Consider the circuit shown in Fig. 2 and answer the following questions: (**Assume  $\gamma = 1$** )
- Find the design values of the resistors of the simple matrix that produce the luminance signal.
  - Roughly, what are the frequency bands occupied by the luminance and color signals?
  - Suggest an adder circuit to produce the color-difference signal(s). Draw a circuit diagram for your suggested solution.
  - Suggest an inverting amplifier. Draw a circuit diagram for your suggested solution.
- Q3. Consider the circuit shown in Fig. 3 and answer the following questions:
- What is the function of the variable resistor  $R_1$ ?
  - What is the function of the variable resistor  $R_2$ ?
  - What is the function of the delay line?
- Q4. **Intermodulation products are always harmful and electronic engineers are always doing their best to get rid of them.**

Discuss this statement and clearly show your position whether you accept it as is or not, always justify your answer and give examples from real life applications to support your answers.

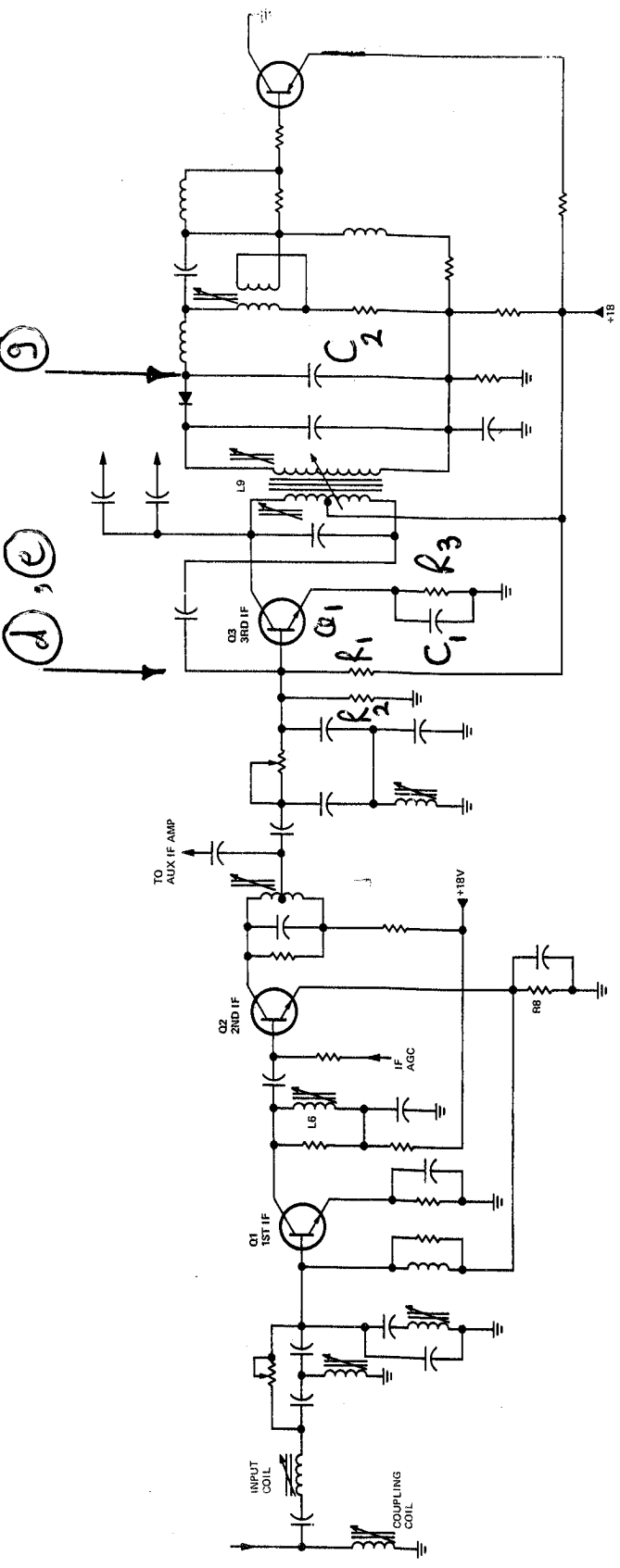


Fig. 1

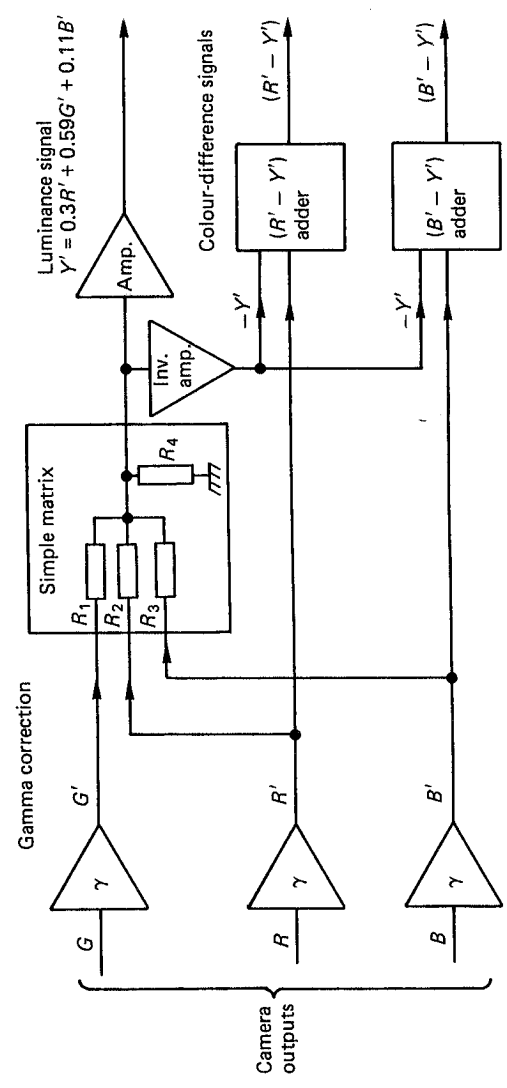


Fig. 2

Q5. You are working for a company that provides consulting services in the area of electronic circuit design. A client wants an amplifier to feed a  $50\ \Omega$  load with 1 kW at a frequency = 100 MHz. You are requested to submit a brief proposal to this client to help him select the most suitable design. What are the main points you should consider in your proposal? Based on these main points which circuit design you would suggest in your proposal?

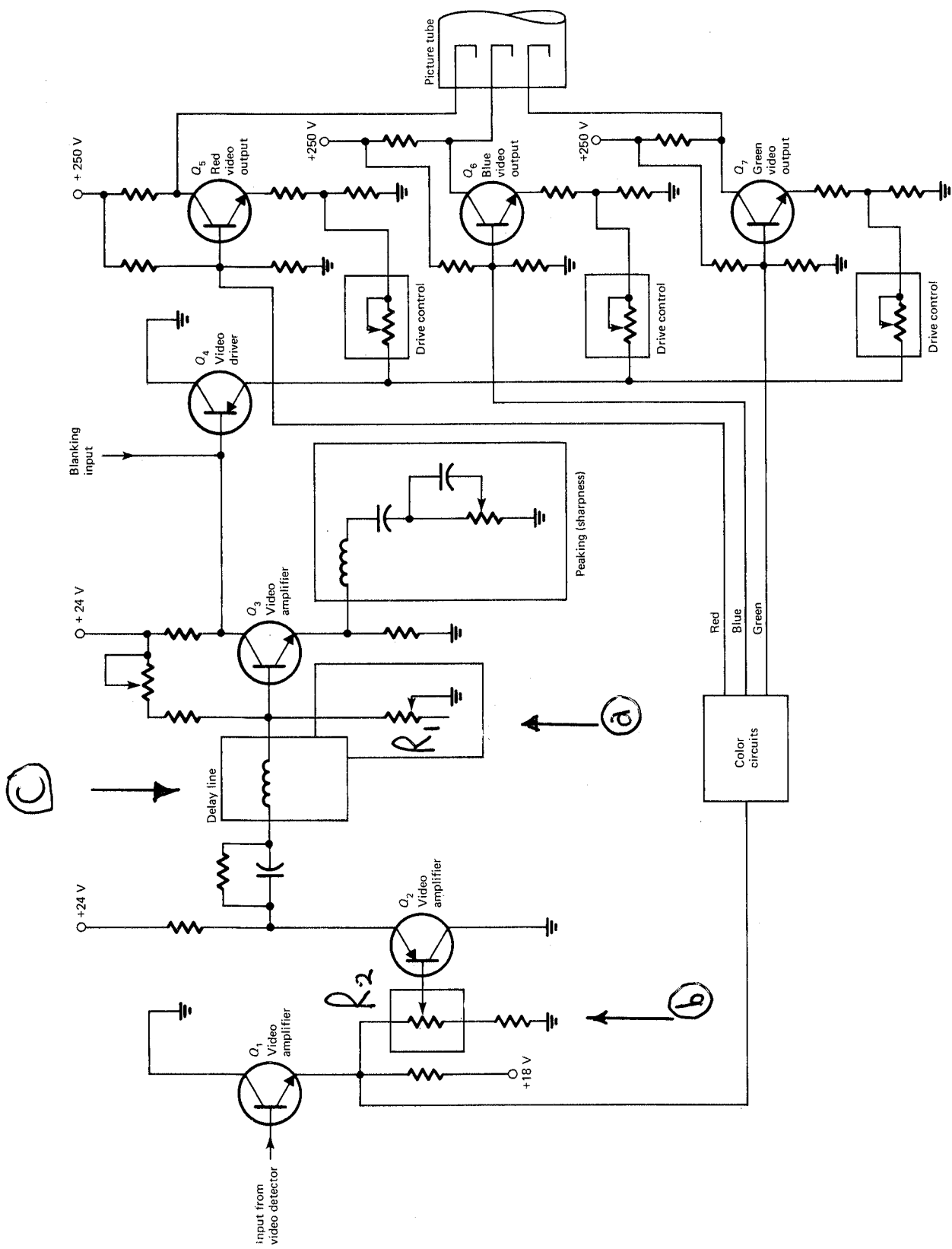


Fig-3