

Section 3

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design

Quiz # 5

28 November 2007

Student's Name:

I.D. #:

Implement the following function with an 8x1 multiplexer. Connect inputs a, C and D to the selection lines S2, S1, and S0. The input requirements for the four data lines will be functions of the remaining variable B.

$$F(A, B, C, D) = \sum (1, 3, 4, 11, 12, 13, 14, 15)$$

Do not write below this line

Instructor : Dr. M.M. Dawoud

(071)

Score : /10