

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design	Quiz #4 (25/4/2007)	
Student's Name :		I.D.# :

Implement the following two function using decoders and OR gates

$$F_1(A, B, C, D) = A'BC + B'D' + AB'C'D$$

$$F_2(A, B, C, D) = \Pi(4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15)$$

1. How many decoders are you going to use?
 2. How many OR gates are you going to use?
 3. Draw a block diagram showing the connections of the decoders and gates
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Don't write below this line.