

KING FAHD UNIVERSITY OF PETROLEUM & MINERALS
Department of Electrical Engineering

EE200 Digital Logic Circuit Design	Quiz #3 (7/4/2007)	
Student's Name :		I.D.# :

Design a combinational circuit that multiplies a one bit binary number x by a 2-bit binary number yz , using AND, OR, INVERT gates. (Follow the general combinational logic design procedure.)

Do not write below this line