

**KING FAHD UNIVERSITY OF PETROLEUM & MINERALS**  
**Department of Electrical Engineering**

<b>EE200 Digital Logic Circuit Design</b>	<b>Quiz #5 (26/10/2005)</b>	<b>Section: 03</b>
<b>Student's Name :</b>		<b>I.D.# :</b>

Implement the following two Boolean functions using decoders and OR gates.

$$F_1(x, y, z) = x'y + xz$$

$$F_2(x, y, z) = \sum(0,1,7)$$

1. How many decoders are you going to use?
2. How many OR gates are you going to use?
3. Draw a block diagram showing the connections of the decoders and gates.

---

Do not write below this line