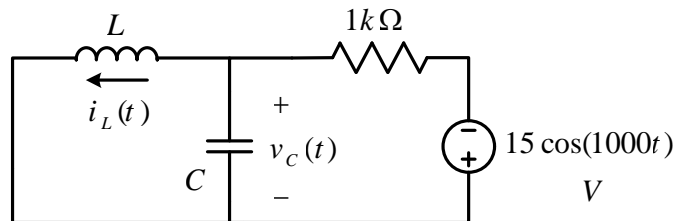EE 201 (04) Design Project
Second Semester 2007/2008

The steady state sinusoidal circuit shown below needs to be designed in order to satisfy the following specifications:

1- The capacitor voltage $v_c(t)$ must have must have an *amplitude* of $6V$.

2- The current $i_L(t)$ through the inductor must have an *amplitude* of $100mA$.

Design the above circuit to satisfy the above two specifications by finding appropriate values of the inductance L and capacitance C . Summarize your results in the table provided below.

Other Requirements:

1- Show all work detail.
2- Work Alone. You can only discuss the design project with the course instructor. *An automatic zero* will be given if this rule is *violated*.
3- Deadline for project submission is Wednesday, May, 28, 2008.
4- The report *must be type-written* and *neatly* prepared.

[Hint: More than one value of C may satisfy the design requirements].

$C (F)$	$L (H)$