

King Fahd University of Petroleum and Minerals
Electrical Engineering Department

EE303 Electronics II

Exam # II

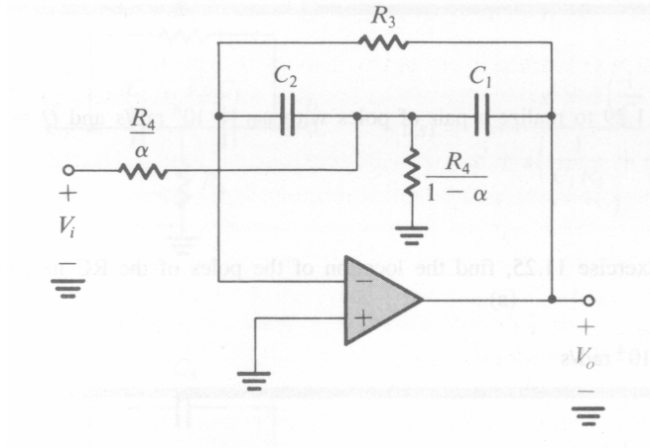
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Question No.1

Assume the op amp is ideal.

- (a) Drive the voltage transfer function for the bandpass filter shown assuming $C_1=C_2$.
- (b) With $C_1=C_2=5\text{nF}$, Design the filter such that the center frequency is 12.5kHz , $Q=10$ and the magnitude of the midband gain is 20.

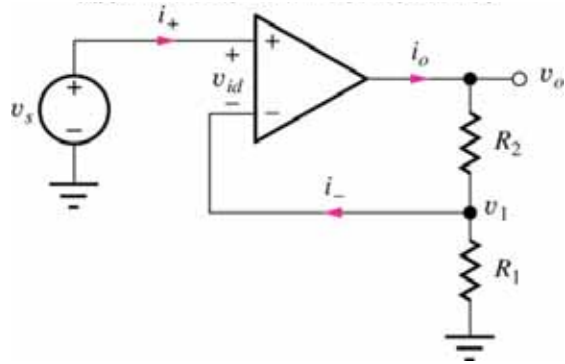


Question No.2

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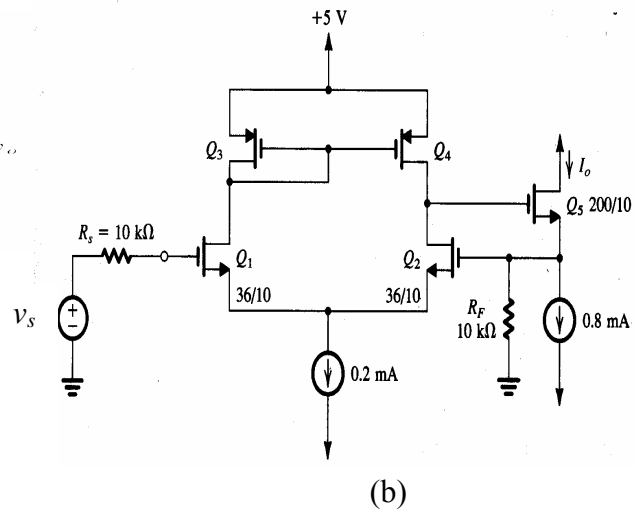
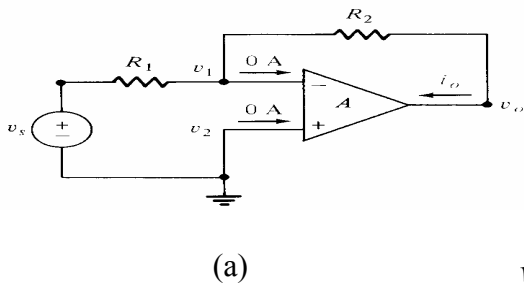
Consider the amplifier in Fig. 2. The op-amp parameters are $A_v=5000$, $R_i=1k\Omega$, and $R_o=100\Omega$. Use $R_2=10R_1=10k\Omega$.

- (a) Which type of feedback topology is incorporated?
- (b) Use feedback analysis to find values for closed loop gain v_o/v_s and input and output resistances.



Question No.3

(a) Determine the type of feedback and calculate the feedback factor β for each amplifier shown.



(b) List three different negative feedback topologies that can be obtained from a single transistor amplifier. Draw these amplifiers.