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PCB LAYOUT GENERATION FROM RTL SPECIFICATIONS

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Abstract

Automatic printed circuit board (PCB) layout generation is currently achieved through the use of several independent CAD systems. A register transfer level (RTL) based design automation system provides a viable alternative to existing methods of PCB layout generation. In this paper, the design and implementation of a software processor that generates a PCB layout using SSI/MSI components from the logical circuit provided by UAHPL, an RTL language, is presented.

Keywords: printed circuit boards | computer aided design | logic circuits | programming languages