

COE 308 – Computer Architecture

Term 072 – Spring 2008

Pipelined Processor Project Evaluation Form

	Student Name / ID	Grade	Remarks	Bonus/Penalty
Members				

	Section	Grade	Max	Remarks
Components	ALU Implementation		8	
	Register File		7	
Instruction Implementation	Logical Instructions (AND, OR, ...)		5	
	Arithmetic (ADD, SUB, ...)		5	
	Load & Store (LW, SW)		5	
	Branch (BEQ, BNE)		5	
	Jumps (J, JAL, JR)		5	
Tool	Ability to use LogiSim effectively		10	
Pipelining	Forwarding implemented properly		10	
	Stalling pipeline (load delay, branch)		10	
Teamwork	Team cooperation and coordination		10	
Report	Writing Style, Spelling, Grammar		5	
	Design Description & Drawings		5	
	Enough Test Cases provided		5	
	Program execution and Discussion		5	