

COE 205 : Computer Organization & Assembly Language  
Quiz on CPU Design – Monday, January 2, 2006

Name:

ID:

**a.** (10 pts) Draw the data path of a single-bus CPU design with 4 general-purpose registers

**b.** (5 pts) Write the fetch control sequence for fetching an instruction from memory

**c.** (5 pts) Write the execution control sequence for executing: SUB R3, R1