

COE 202 – Digital Logic Design

Term 081 – Fall 2008

Project Evaluation Form

	Student Name / ID	Grade	Remarks
Members			

	Section	Grade	Max	Remarks
Datapath Implementation	Counter		10	
	Shift Register		10	
	Putting it all together		10	
7-Segment Display	Display and Decoder are implemented properly		10	
Overall Design	Datapath and Controller are connected properly and tested		20	
Report	Writing Style, Spelling, Grammar		5	
	Design Description and Testing		15	