

King Fahd University of Petroleum and Minerals
College of Computer Sciences and Engineering
Department of Computer Engineering
COE 202 – Fundamentals of Computer Engineering

(Term: 071, Section 2)

Instructor: Dr. Basem AL-Madani (mbasem@kfupm.edu.sa)

Computer Aided Design Assignment # 03

Due date & time: 15th Jan. 2008 class time

Design a sequential circuit that takes a stream of binary bits and recognizes the sequence of bits “1001”. The circuit should activate an output z with the detection of the last 1 in the required sequence. Use JK-Type Flip-Flop and D-type Flip-Flop.

Consider Moore State Machine in your design.

Important:

- 1- Save your file as “COE202sec02term071_CAD03_*yourID*.cct”
- 2- Send the file to the grader’s email (dz_razorblade87@hotmail.com) and cc to me (mbasem@kfupm.edu.sa)
- 3- Test your design carefully and do not send several versions of your design.
ONLY THE FIRST VERSION WILL BE CONSIDERED.
- 4- Submit a hard copy showing all your design steps. **Credit will be given for each design steps.**