

COE 203

HW1 (on Sep. 9 '07)

Due Next Lab. (on Sep. 16 '07)

Design a digital logic circuit using multiplexers which should have 4 inputs (A,B,C,D) and 2 output bits (F,G). Whenever the number of 1's is less than the number of 0's, $F=1$, $G=0$; otherwise $F=0$, $G=1$.