## COE 301/ICS 233, Term 172

## Computer Architecture \& Assembly Language

## HW\# 4

Q.1. Write a MIPS assembly program to perform signed multiplication of 32-bit numbers using the algorithm studied in class. The program should ask the user to inter two integers and then display the result of multiplication. If the result cannot fit in 32-bit then the program should indicate that there is overflow. Test your program using the following numbers:

1. $-1 \mathrm{x}-1$
2. $100 \mathrm{x}-2$
3. $0 \times 10$
4. 2147483647 x 2

A sample execution of the program is shown below:

Enter the multiplier: 100
Enter the multiplicand: -2
Result of multiplication $=-200$
Q.2. Write a MIPS assembly program to perform signed division of 32-bit numbers using the algorithm studied in class. The program should ask the user to inter two integers and then display the result of division displaying both the quotient and remainder. Test your program using the following numbers:

1. $+17 \div+3$
2. $+17 \div-3$
3. $-17 \div+3$
4. $-17 \div-3$

A sample execution of the program is shown below:
Enter the dividend: 17
Enter the divisor: -3
Result of division: Quotient $=-5$ Remainder $=2$
Q.3. What is the decimal value of the following single-precision floating-point numbers?
(i) 00100000000111000000000000000000
(ii) 11001111111010000000000000000000
Q.4. Show the IEEE 754 binary representation for: -24.0625 in ...
(i) Single Precision
(ii) Double precision
Q.5. Perform the following floating-point operations rounding the result to the nearest even. Perform the operation assuming both infinite precision and using only guard, round and sticky bits. Compare your solution in both cases.
(i) 01000011100000000000000000000000 - 01000001100000000000000000001100
(ii) 00111111100000000000000000000000 - 00111111100001000000000000000000
(iii) $\quad 00111111111111111111111111111110$ $+00110100010000000000000000000000$
Q.6. Given that $x=01011111101111100100000000000000$ $y=00111111111110000000000000000000$ $z=11011111101111100100000000000000$
represent single precision IEEE 754 floating-point numbers. Perform the following operations showing all work:
(i) $x+y$
(ii) Result of (i) $+z$
(iii) Why is the result of (ii) counterintuitive?

