

SN54LS08, SN74LS08 QUADRUPLE 2-INPUT POSITIVE-AND GATES

recommended operating conditions

	SN54LS08			SN74LS08			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V _{CC} Supply voltage	4.5	5	5.5	4.75	5	5.25	V
V _{IH} High-level input voltage	2			2			V
V _{IL} Low-level input voltage			0.7			0.8	V
I _{OH} High-level output current			-0.4			-0.4	mA
I _{OL} Low-level output current			4			8	mA
T _A Operating free-air temperature	-55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †	SN54LS08		SN74LS08		UNIT		
		MIN	TYP‡	MAX	MIN		TYP‡	MAX
V _{IK}	V _{CC} = MIN, I _I = -18 mA			-1.5		-1.5	V	
V _{OH}	V _{CC} = MIN, V _{IH} = 2 V, I _{OH} = -0.4 mA	2.5	3.4	2.7	3.4		V	
V _{OL}	V _{CC} = MIN, V _{IL} = MAX, I _{OL} = 4 mA	0.25	0.4	0.25	0.4		V	
	V _{CC} = MIN, V _{IL} = MAX, I _{OL} = 8 mA			0.35	0.5			
I _I	V _{CC} = MAX, V _I = 7 V		0.1			0.1	mA	
I _{IH}	V _{CC} = MAX, V _I = 2.7 V		20			20	μA	
I _{IL}	V _{CC} = MAX, V _I = 0.4 V		-0.4			-0.4	mA	
I _{OS} §	V _{CC} = MAX	-20		-100		-20	-100	mA
I _{CCH}	V _{CC} = MAX, V _I = 4.5 V		2.4	4.8		2.4	4.8	mA
I _{CCL}	V _{CC} = MAX, V _I = 0 V		4.4	8.8		4.4	8.8	mA

† For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

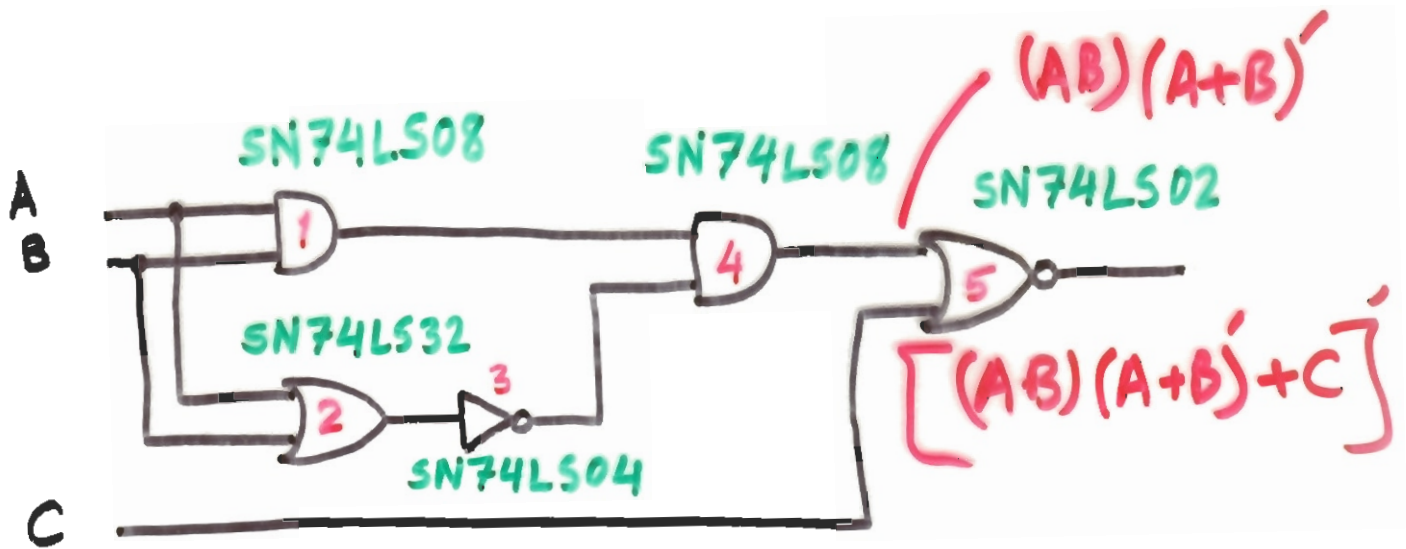
‡ All typical values are at V_{CC} = 5 V, T_A = 25°C

§ Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS		MIN	TYP	MAX	UNIT
I _{PLH}	A or B	Y	R _L = 2 kΩ,	C _L = 15 pF		8	15	ns
I _{PHL}						10	20	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



3 paths from input to output:

- (1) 1 → 4 → 5
- (2) 2 → 3 → 4 → 5
- (3) 5

* Propagational Delay:

$$\begin{aligned}
 (1) & \text{Max}(t_{PLH_{max}}, t_{PHL_{max}})_{08} + \text{Max}(t_{PLH_{max}}, t_{PHL_{max}})_{08} \\
 & + \text{Max}(t_{PLH_{max}}, t_{PHL_{max}})_{02} \\
 & = \text{Max}(15 \text{ ns}, 20 \text{ ns}) + \text{Max}(15 \text{ ns}, 20 \text{ ns}) + \text{Max}(15 \text{ ns}, 15 \text{ ns}) \\
 & = 20 \text{ ns} + 20 \text{ ns} + 15 \text{ ns} = 55 \text{ ns}
 \end{aligned}$$

$$\begin{aligned}
 (2) & \text{Max}(22 \text{ ns}, 22 \text{ ns}) + \text{Max}(15, 15) + \text{Max}(15, 20) + \text{Max}(15, 15) \\
 & = 22 \text{ ns} + 15 \text{ ns} + 20 \text{ ns} + 15 \text{ ns} \\
 & = 72 \text{ ns}
 \end{aligned}$$

$$(3) \text{Max}(15 \text{ ns}, 15 \text{ ns}) = 15 \text{ ns}$$

⇒ Worst-case propagational delay =
 $\text{Max}(\text{case 1, case 2, case 3})$

$$= \text{Max}(55 \text{ ns}, 72 \text{ ns}, 15 \text{ ns}) = \underline{\underline{72 \text{ ns}}}$$