

King Fahd University of Petroleum and Minerals
College of Computer Science and Engineering
Computer Engineering Department

COE 202: Digital Logic Design (3-0-3)
Term 122 (Spring 2013)
Major Exam 1
Thursday February 28, 2013

Time: 90 minutes, Total Pages: 8

Name: _____ **ID:** _____ **Section:** _____

Notes:

- Do not open the exam book until instructed
- **Calculators are not allowed** (*basic, advanced, cell phones, etc.*)
- Answer all questions
- All steps must be shown
- Any assumptions made must be clearly stated

Question	Maximum Points	Your Points
1	25	
2	15	
3	10	
4	10	
Total	60	

Question 1.**(25 points)**

a. Perform the following arithmetic operations in the specified number system.

(8 points)

Binary Subtraction	Hexadecimal Addition	Binary Addition	Octal Multiplication
$\begin{array}{r} 1000.10 \\ -0111.01 \\ \hline \end{array}$	$\begin{array}{r} 2F3A \\ +4BC2 \\ \hline \end{array}$	$\begin{array}{r} 1001.11 \\ +0110.01 \\ \hline \end{array}$	$\begin{array}{r} 1271 \\ \times 12 \\ \hline \end{array}$

b. Give the binary, octal, and hexadecimal representations of the decimal value **130**. **(4 points)**

- c. Suppose that we want to represent the decimal fraction 0.6.
- I. Find the 4-bit binary fraction that gives the closest value.
 - II. Find the 2-Digit Hexadecimal fraction that gives the closest value.
 - III. What do you observe?
 - IV. Without calculations, give the 4-digit Octal representation.

(7 points)

d. Write the sequence of hexadecimal numbers from 9F7 to A03.

(2 points)

e. There are 26 letters in the English alphabet. Each letter has two possible shapes capital and small.

Answer the following.

(4 points)

- I. If you are to assign a binary code to represent only these letters, what is the minimum number of bits needed for such encoding
- II. Suggest an encoding that makes the conversion process between capital and small letters the easiest. In your suggested encoding, give the binary codes of 'A', 'B', 'Z', 'a', 'b', and 'z'.

Question 2.

(15 points)

Using Algebraic manipulation:

- i. (5 points) Simplify the function $F(X, Y, Z) = \bar{X} \bar{Y} \bar{Z} + \bar{X} \bar{Y} Z + \bar{X} Y Z + X \bar{Y} Z + X Y Z$ to three literals.

- ii. (5 points) Show that the function $F(A, B, C) = \bar{A} \bar{B} + A C + B \bar{C}$ is equal to the function $G(A, B, C) = A B + \bar{A} \bar{C} + \bar{B} C$ by starting from F and reaching to G.

- iii. (5 points) Provide a simplified sum-of-product (SOP) expression for the complement of the function: $F(A, B, C, D) = (A + \bar{B} C)\bar{D} + (\bar{A} + \bar{C})(B + D)$

(10 points)

Question 3.

Consider the logic function $F(X, Y, Z) = Y\bar{Z} + (Y + Z)(\bar{X}Y)$

a. Give the logic diagram for the function F as given above (without simplifications)

b. Express $F(X, Y, Z)$ as a sum of products

c. Express F as a sum of minterms in the form $F = \sum m(\dots)$

d. Fill in the column for F in the truth table shown

X	Y	Z	F(X,Y,Z)	G(X,Y,Z)
0	0	0		1
0	0	1		1
0	1	0		0
0	1	1		1
1	0	0		1
1	0	1		0
1	1	0		1
1	1	1		0

e. For the function $G(X, Y, Z)$ in the truth table above:

- Express G as a product of maxterms in the form $G = \prod M(\dots)$

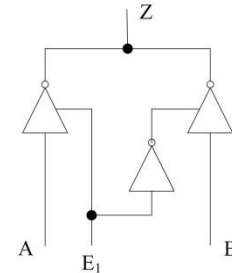
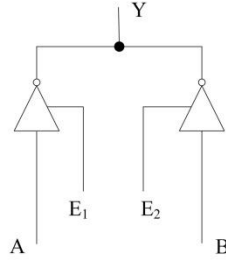
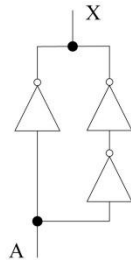
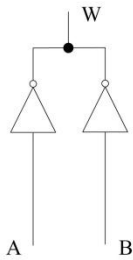
- Express G as an *algebraic* product of Maxterms, $G(X, Y, Z) = \underline{\hspace{10em}}$

- $\bar{G}(X, Y, Z) = \prod M(\underline{\hspace{10em}})$

Question 4.

(10 points)

- a. For each of the following four circuits, indicate whether the circuit is valid or not by circling (T) when valid or (F) when Not *where* A, B, E₁, and E₂ are independent of one another and may assume any possible binary values.
- b. For each valid circuit, give the Boolean expression of its output.



Valid: T F

Valid: T F

Valid: T F

Valid: T F

W = _____

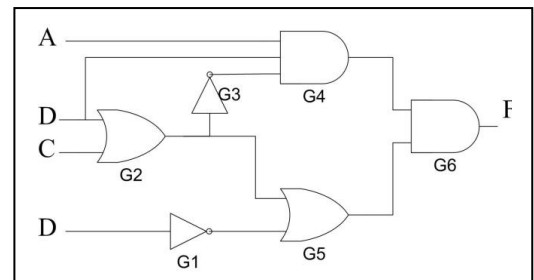
X = _____

Y = _____

Z = _____

- c. Given the propagation delay of the basic gates; INVERTERS 1ns each, AND gates 3ns each and OR gates 5ns each answer the following:

- i. What is the critical path delay (longest propagation delay along a path in this circuit)? What is the maximum frequency at which the circuit may be operated?



ii. The largest gate fanin is _____ of gate _____

iii. The gate that is driving the largest load is gate _____