

COE 202, Term 131

Digital Logic Design

Assignment# 1

Due date: Tuesday, Nov. 12

- Q.1.** It is required to design a combinational circuit that computes the equation $Y=3*X$, where X is an n -bit unsigned number.
- (i) Design the circuit as a modular circuit where each module receives a single bit of the input, X_i .
 - (ii) Derive the truth table of your 1-bit module in (i).
 - (iii) Derive minimized two-level sum-of-product equations for your 1-bit module circuit.
 - (iv) Verify the correctness of your design by modeling and simulating a 4-bit circuit using LogicWorks.

This assignment can be solved based on a group of two students. Include snapshots of simulation output to illustrate the correctness of your circuit. Submit your solution as a word document along with the circuit in one zipped file.