

Name:

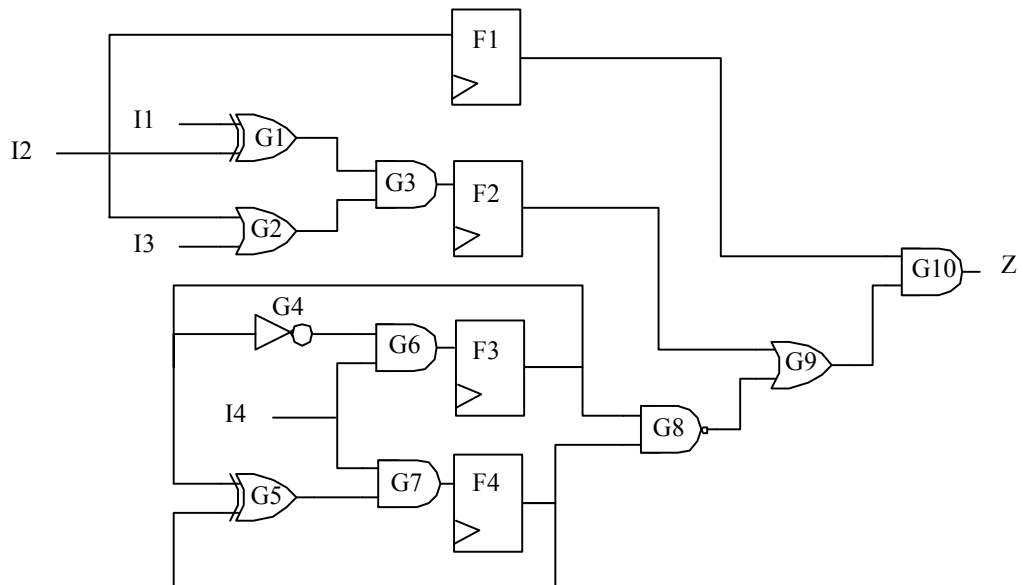
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COE 464, Term 042
Testing of Digital Circuits

Quiz# 6

Date: Sunday, May 29, 2005

Q1. Consider the sequential circuit shown below, which has four primary inputs, namely I1, I2, I3, I4, and one primary output, Z, and four D-FFs



- i. Convert the given sequential circuit into a **Full-Scan** circuit using **Multiplexed-Data Flip Flop (MD-FF)** scan cell.
- ii. Using the full-scan circuit derived in (i), generate a test for the fault **G7 stuck-at-1**.

